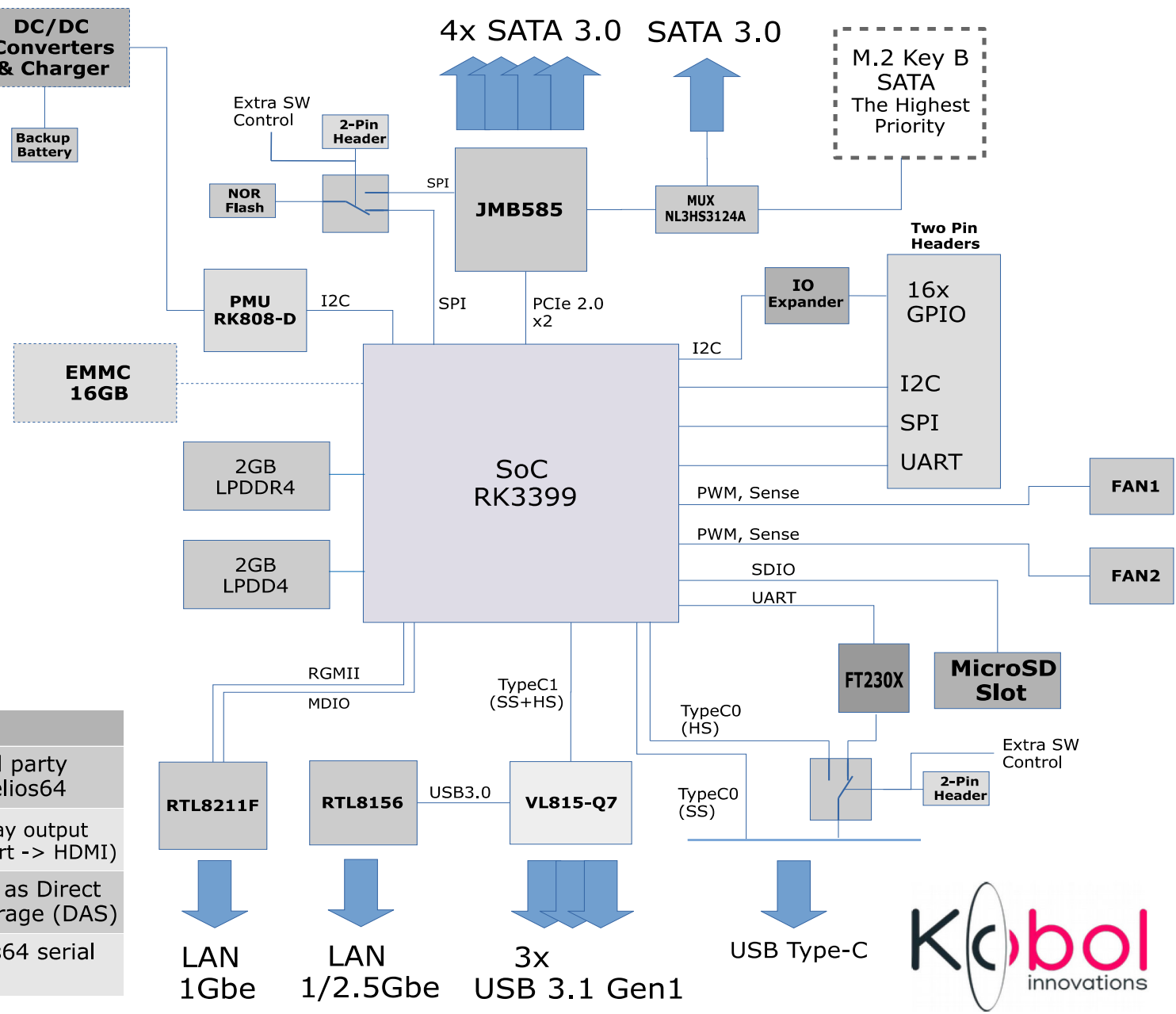


LED	Name
G	Sys Power
G	USB/Peripheral Power
G	HDD Power
Y	Battery Status
B x5	SATA Activity
R x5	SATA Error
G	M.2 SATA Activity
G x4	USB Activity
R	System Error
B	System Status
Ext1	HDD
Ext2	PWR

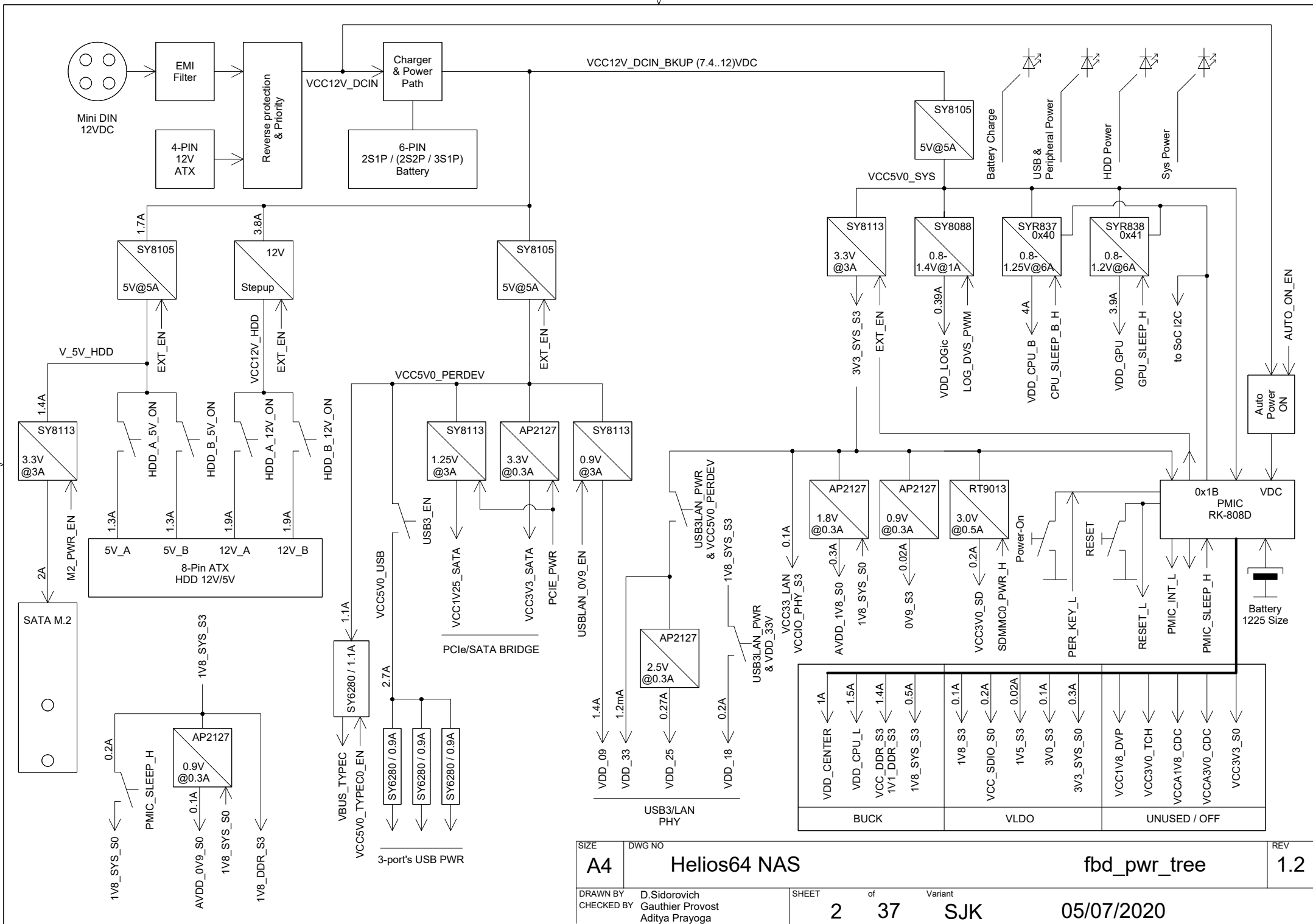
USB Type-C usage

Mode	Description
USB 3.0 Host	Connect third party devices to Helios64
DisplayPort	Helios64 display output (e.g DisplayPort -> HDMI)
USB 3.0 Device	Use Helios64 as Direct Attached Storage (DAS)
USB 2.0 Device	Access Helios64 serial port via FTDI



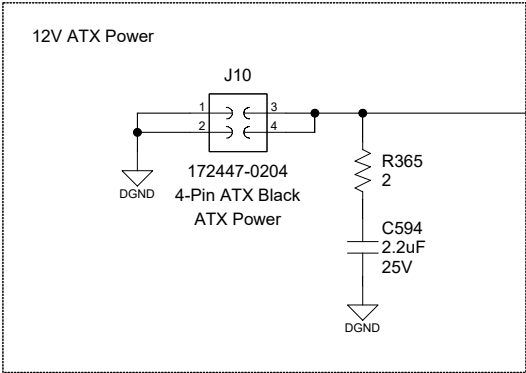
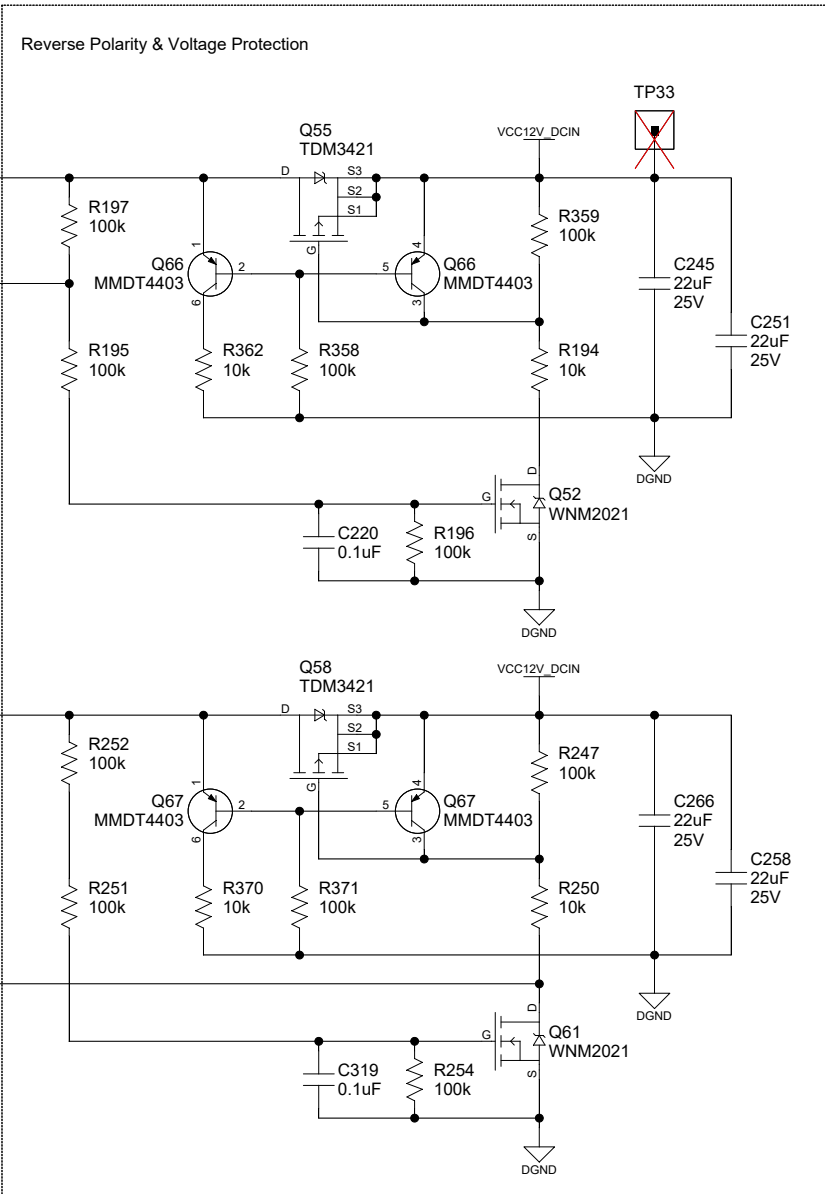
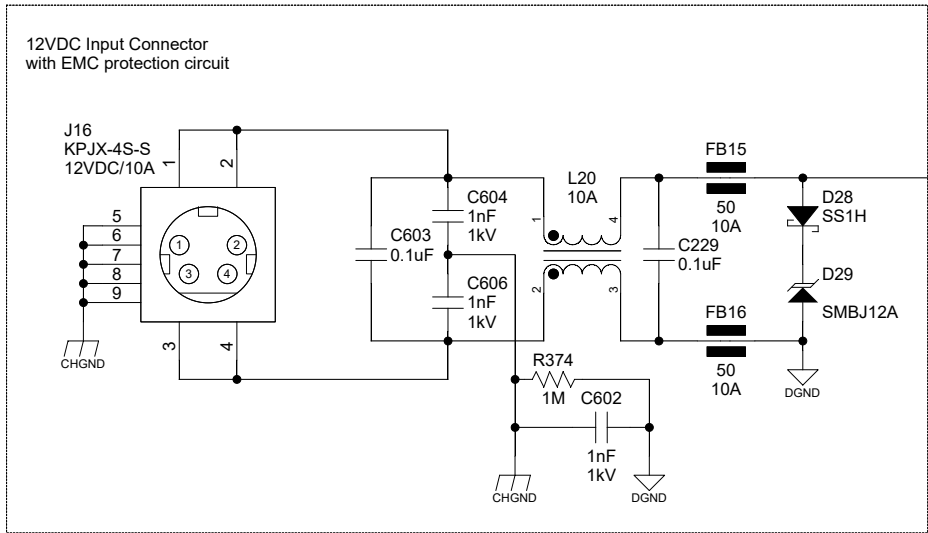
SIZE A4	DWG NO Helios64 NAS	SHEET 1 of 37		Variant SJK	REV 1.2
DRAWN BY D.Sidorovich		CHECKED BY Gauthier Provost Aditya Prayoga		fbd_sys 05/07/2020	

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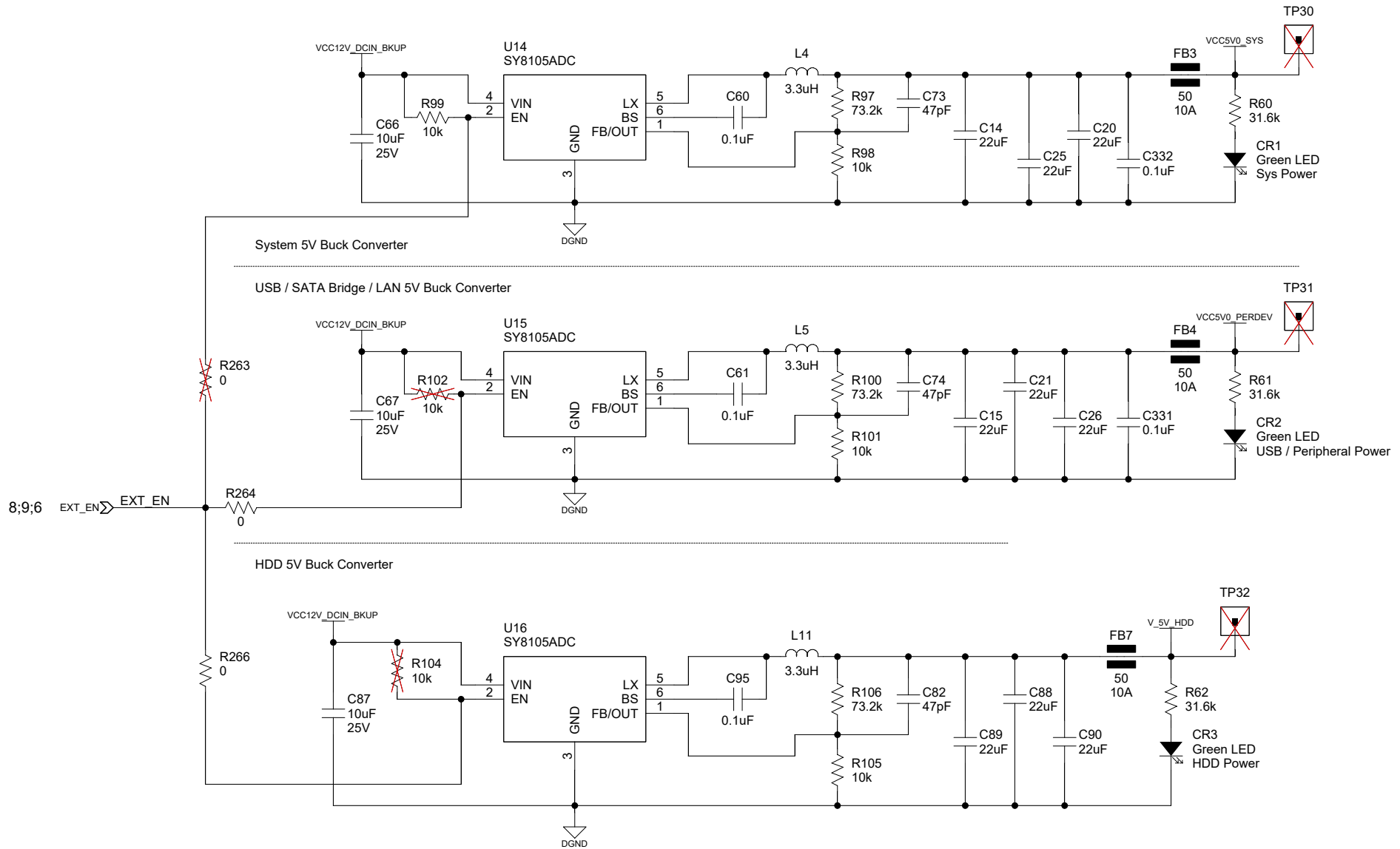
SIZE A4	DWG NO Helios64 NAS	SHEET 2 of 37 Variant SJK		REV 1.2
DRAWN BY D.Sidorovich		CHECKED BY Gauthier Provost Aditya Prayoga		DATE 05/07/2020
fbdpwr_tree				

DC Power Input



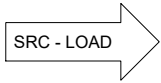
P15	Description
Open	O-Ring Connection
Close	Set ATX Priority

Main DCDC Converters

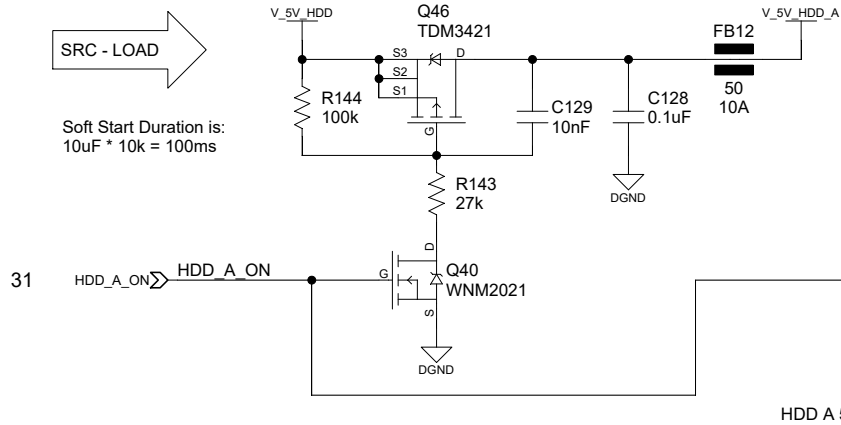


SIZE	DWG NO			REV
A4	Helios64 NAS	sys_usb_hdd_pwr		1.2
DRAWN BY	D.Sidorovich	SHEET	of	Variant
CHECKED BY	Gauthier Provost Aditya Prayoga	4	37	SJK
			05/16/2020	

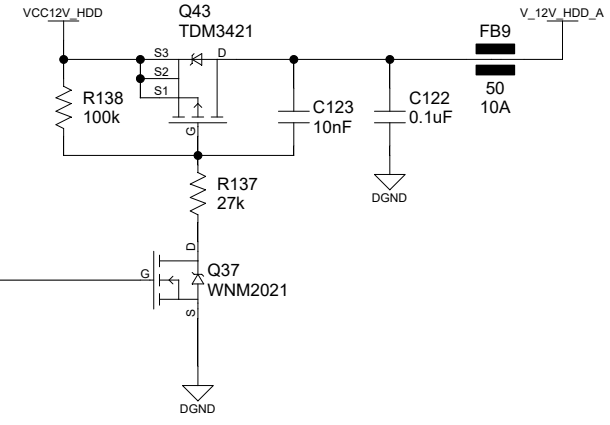
HDD Power Switches



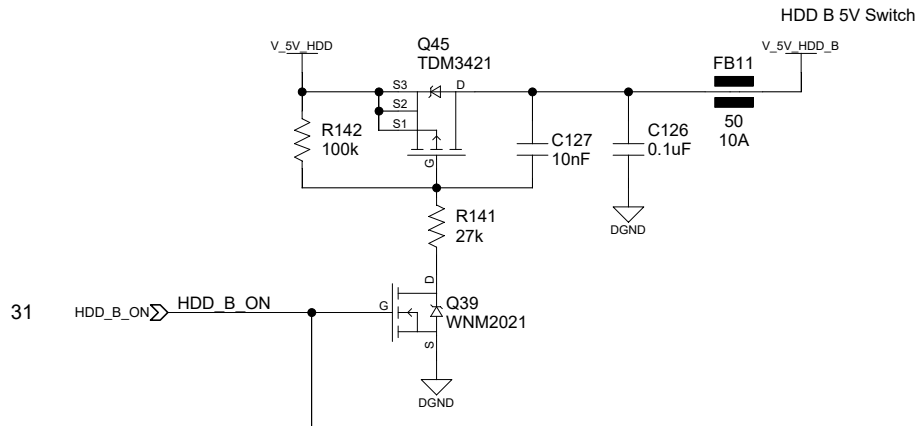
Soft Start Duration is:
 $10\mu\text{F} * 10\text{k} = 100\text{ms}$



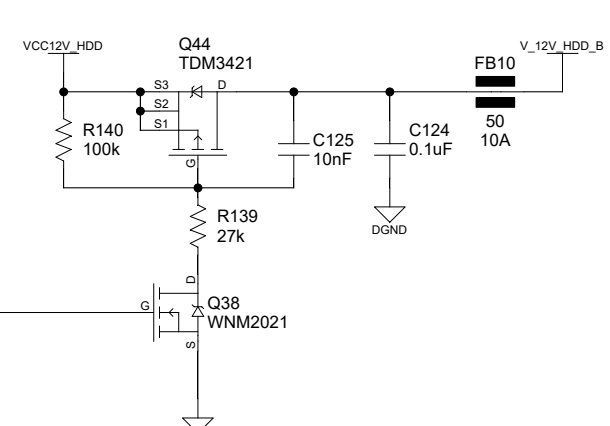
HDD A 5V Switch



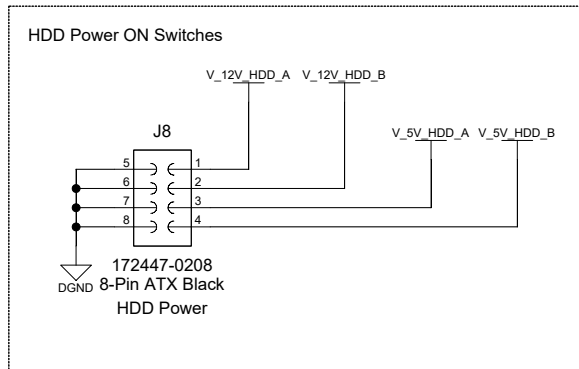
HDD A 12V Switch



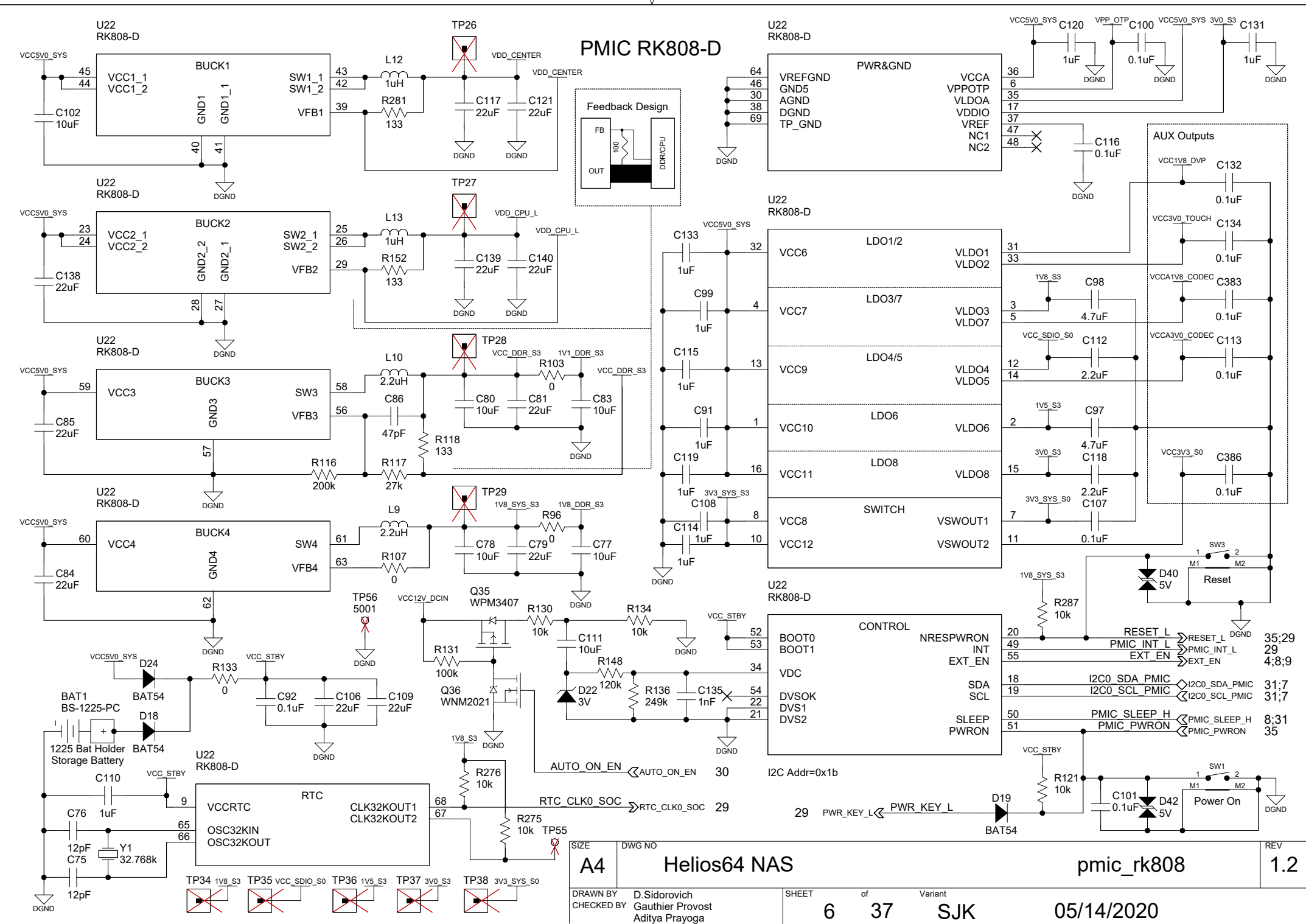
HDD B 5V Switch



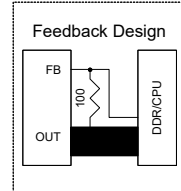
HDD B 12V Switch



SIZE	DWG NO	SHEET		of	Variant	REV
A4	Helios64 NAS	5	37		SJK	1.2
DRAWN BY D.Sidorovich		CHECKED BY Gauthier Provost Aditya Prayoga		05/13/2020		hdd_pwr_sw



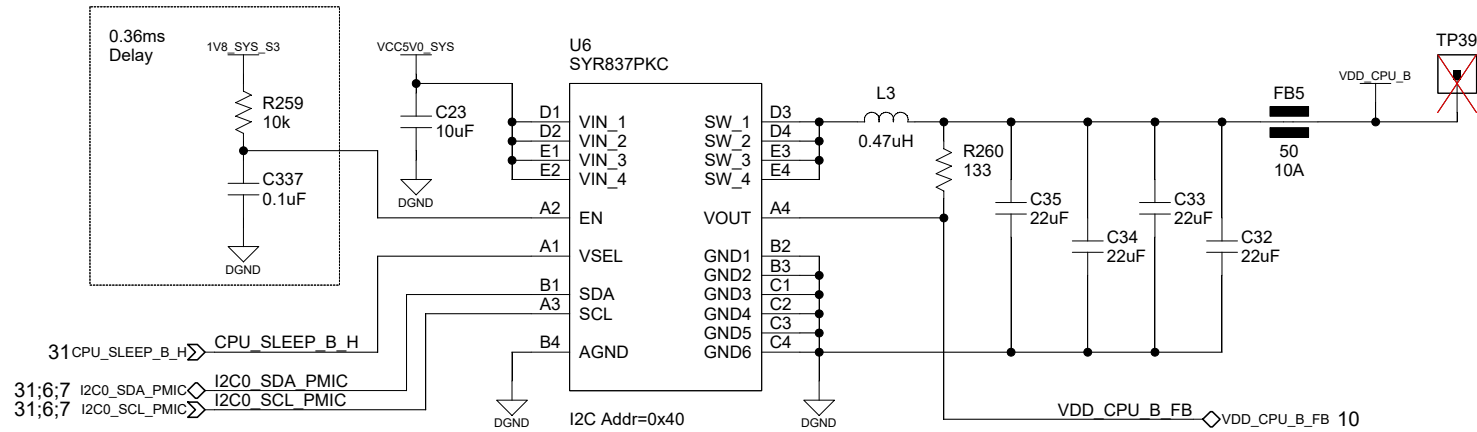
PMIC RK808-D



SIZE	DWG NO	SHEET		Variant	REV
A4	Helios64 NAS	6	37	SJK	1.2
DRAWN BY D.Sidorovich		SHEET		Variant	REV
CHECKED BY Gauthier Provost		6	37	SJK	05/14/2020
Aditya Prayoga					

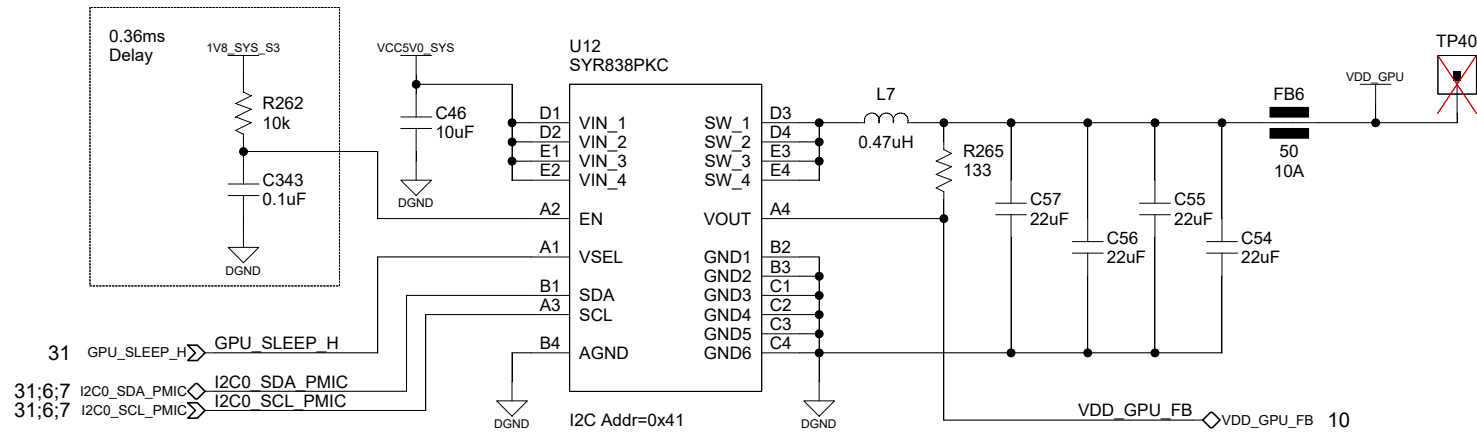
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CPU & GPU Core Power



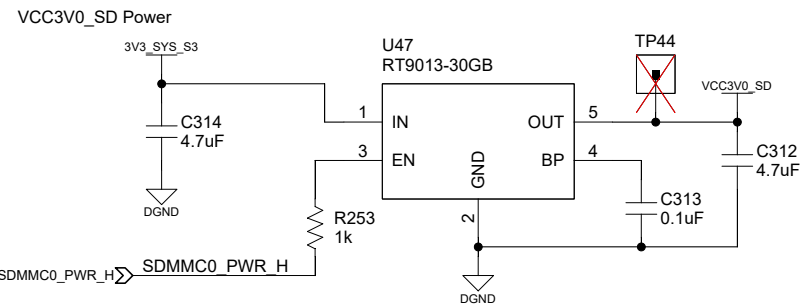
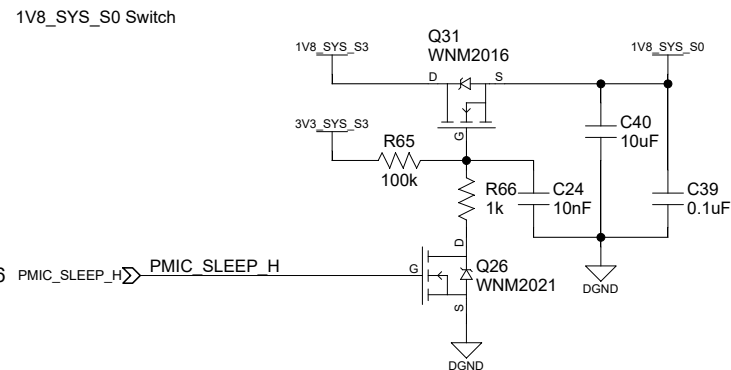
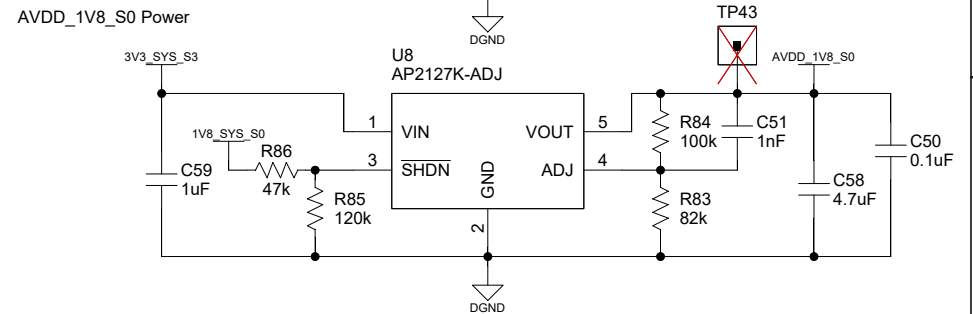
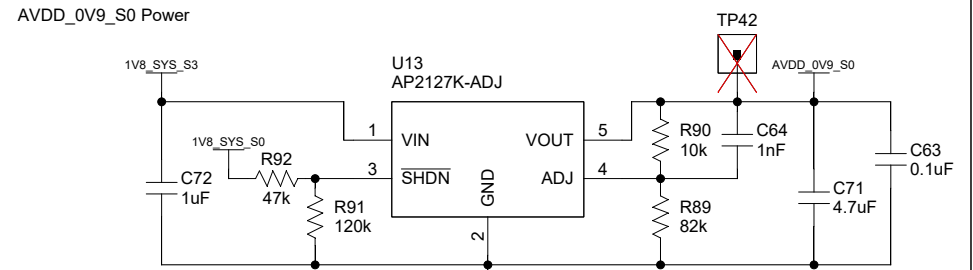
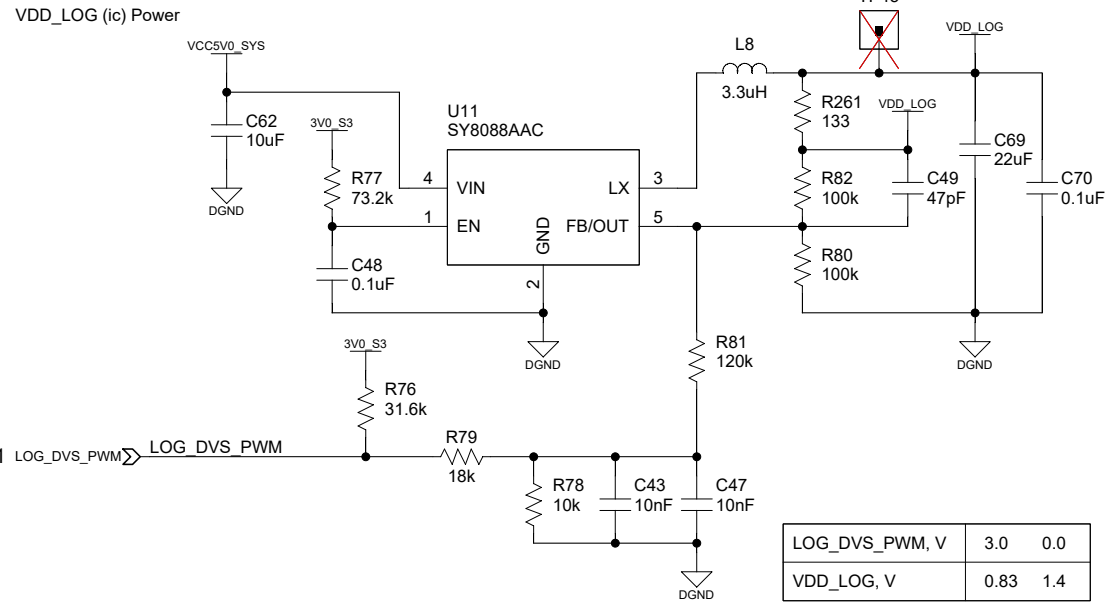
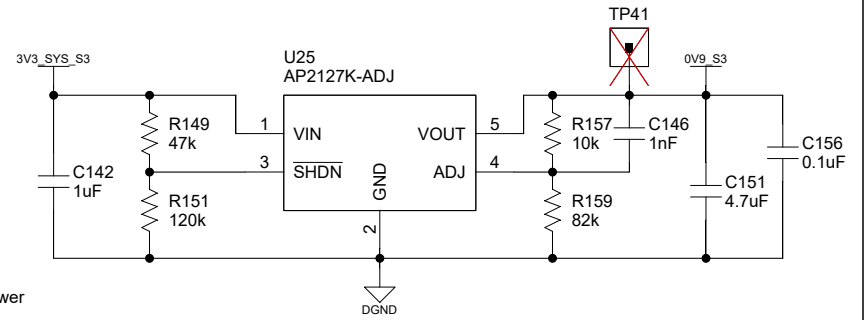
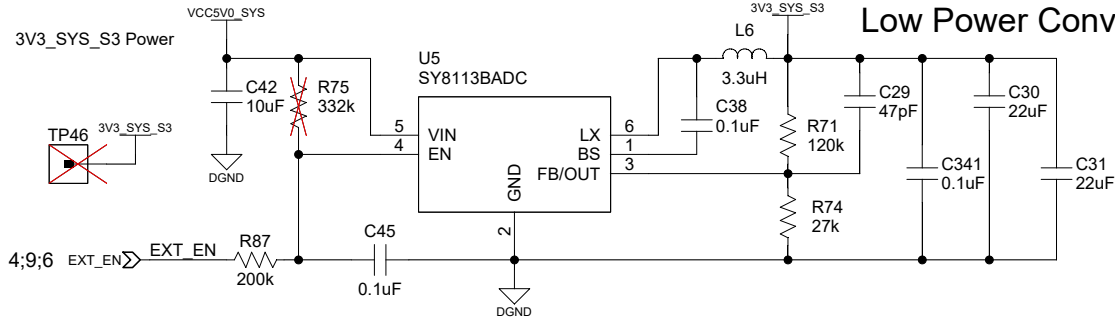
VDD_CPU_B Power

VDD_GPU Power

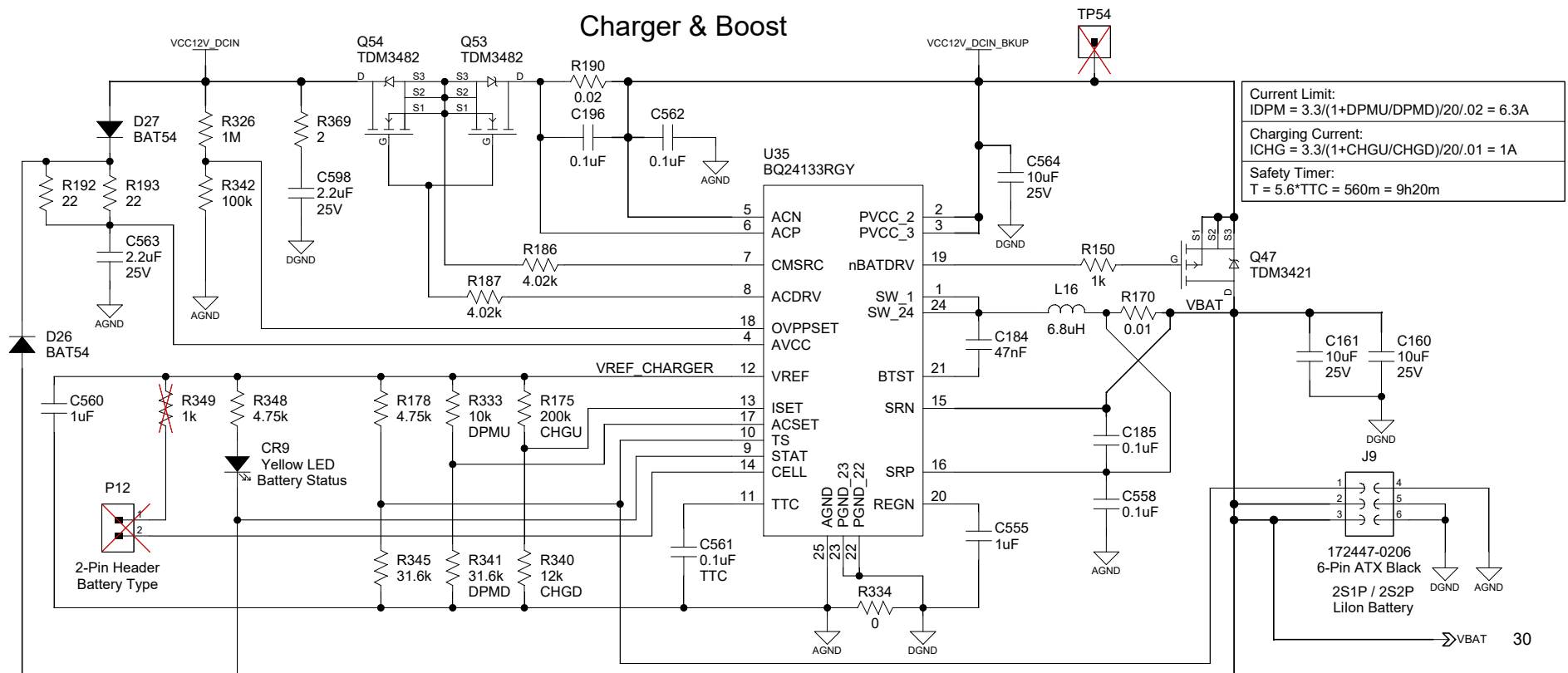


SIZE	DWG NO			REV
A4	Helios64 NAS	cpu_gpu_pwr		1.2
DRAWN BY	D.Sidorovich	SHEET	of	Variant
CHECKED BY	Gauthier Provost Aditya Prayoga	7	37	SJK
			05/15/2020	

Low Power Converters

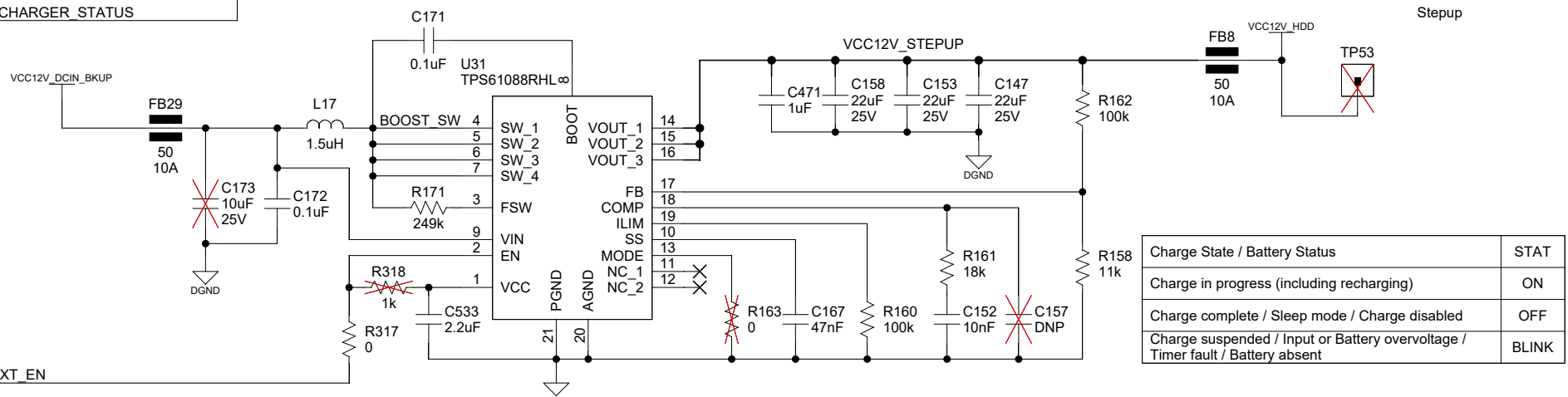


Charger & Boost



Current Limit:
 $IDPM = 3.3 / (1 + DPMU / DPMD) / 20 / .02 = 6.3A$
 Charging Current:
 $ICHG = 3.3 / (1 + CHGU / CHGD) / 20 / .01 = 1A$
 Safety Timer:
 $T = 5.6 * TTC = 560m = 9h20m$

15 CHARGER_STATUS ← CHARGER STATUS



Charge State / Battery Status	STAT
Charge in progress (including recharging)	ON
Charge complete / Sleep mode / Charge disabled	OFF
Charge suspended / Input or Battery overvoltage / Timer fault / Battery absent	BLINK

4;8;6 EXT_EN → EXT_EN

SIZE A4	DWG NO Helios64 NAS	SHEET 9	of 37	Variant SJK	REV 1.2
DRAWN BY D.Sidorovich		CHECKED BY Gauthier Provost Aditya Prayoga		DATE 05/15/2020	

U27
RK3399

U27
RK3399

SoC Power

U27
RK3399

Y23
AF23 AVSS_1
AF24 AVSS_2
AA23 AVSS_3
AB23 AVSS_4
AA26 AVSS_5
AA29 AVSS_6
AB11 AVSS_7
AB13 AVSS_8
AC15 AVSS_9
AD13 AVSS_10
AB10 AVSS_11
AA11 AVSS_12
AC26 AVSS_13
AC29 AVSS_14
AD17 AVSS_15
AA12 AVSS_16
AC16 AVSS_17
AC23 AVSS_18
AD29 AVSS_19
AE11 AVSS_20
AE12 AVSS_21
AE14 AVSS_22
AE17 AVSS_23
AE27 AVSS_24
AA14 AVSS_25
AF17 AVSS_26
AF29 AVSS_27
AG29 AVSS_28
AH29 AVSS_29
AJ6 AVSS_30
AJ8 AVSS_31
AJ9 AVSS_32
AJ11 AVSS_33
AJ12 AVSS_34
AJ14 AVSS_35
AJ15 AVSS_36
AJ17 AVSS_37
AJ18 AVSS_38
AD26 AVSS_39
AB16 AVSS_40
AB15 AVSS_41
AC17 AVSS_42
AC11 AVSS_43
AC13 AVSS_44
W23 AVSS_45
AJ29 AVSS_46
U23 AVSS_47
V23 AVSS_48
AC25 AVSS_49
AB17 AVSS_50
AA15 AVSS_51
AVSS_52
AVSS_53

VSS_1
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VSS_60

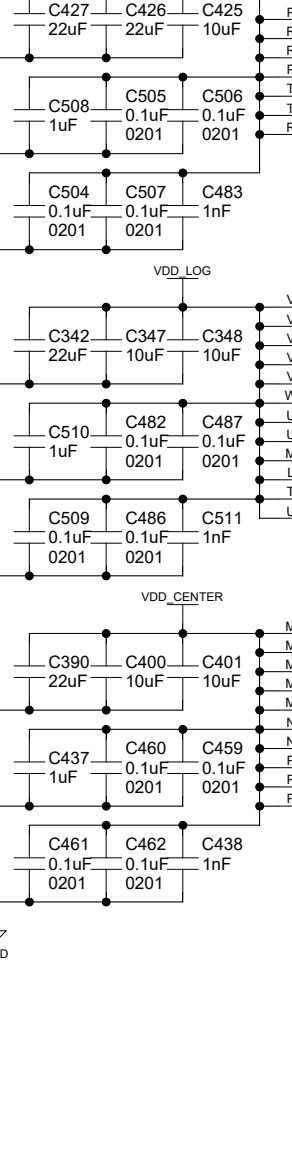
A1 P21
A27 T19
A31 R3
AA3 R5
AA5 R6
AA9 U11
AF18 U12
Y3 W13
AB9 R14
AF20 R15
AD22 R16
AC22 AC21
AC20 R23
AE23 T8
Y10 T10
AC3 Y12
AD3 U14
AD5 T16
AD10 T21
AF9 AB19
AG2 U3
AJ5 U8
AL1 U15
B5 U16
C8 AC18
C9 U19
C11 U21
C12 V17
C14 U22
C15 U29
C17 V3
C18 V5
C20 V8
C21 V10
C23 Y11
C24 Y14
C26 Y15
D5 W22
E2 W17
E4 W30
E7 W8
E12 W9
E15 AD21
E18 Y13
E21 R18
E24 Y9
E31 W19
F8 T18
F15 W18
F18 Y20
F20 AJ28
F21 AJ21
G5 AJ23
G9 AJ24
G18 AJ26
G27 AJ27
H3 AA13
H9 AL31
H10 AA10
H11 AJ20

VSS_121
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VSS_119
VSS_120

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H15
H16
H17
H18
H26
J3
J6
J7
J8
J9
J10
K8
K9
K10
K12
K14
K16
K18
W21
K20
N19
K22
L3
L6
L8
L11
L12
L13
L14
L15
L16
N17
Y17
L20
L22
N21
L27
M3
M8
M10
M16
M23
N8
P11
P12
N13
N14
N15
N16
P3
P6
P7
P8
P10
P16
Y16
P19
R21

VDD_CPU_L
VDD_LOG
VDD_CENTER
VDD_GPU

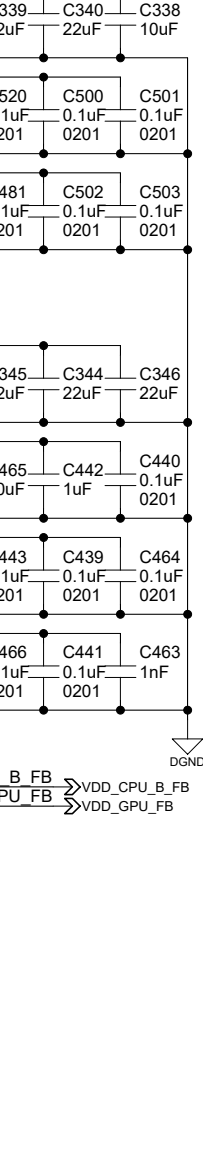


LITCPU_VDD_1
LITCPU_VDD_2
LITCPU_VDD_3
LITCPU_VDD_4
LITCPU_VDD_5
LITCPU_VDD_6
LITCPU_VDD_7

LOGIC_VDD_1
LOGIC_VDD_2
LOGIC_VDD_3
LOGIC_VDD_4
LOGIC_VDD_5
LOGIC_VDD_6
LOGIC_VDD_7
LOGIC_VDD_8
LOGIC_VDD_9
LOGIC_VDD_10
LOGIC_VDD_11
LOGIC_VDD_12

BIGCPU_VDD_1
BIGCPU_VDD_2
BIGCPU_VDD_3
BIGCPU_VDD_4
BIGCPU_VDD_5
BIGCPU_VDD_6
BIGCPU_VDD_7
BIGCPU_VDD_8
BIGCPU_VDD_9
BIGCPU_VDD_10
BIGCPU_VDD_11
BIGCPU_VDD_12
BIGCPU_VDD_13

GPU_VDD_1
GPU_VDD_2
GPU_VDD_3
GPU_VDD_4
GPU_VDD_5
GPU_VDD_6
GPU_VDD_7
GPU_VDD_8
GPU_VDD_9
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GPU_VDD_11
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GPU_VDD_13
GPU_VDD_14
GPU_VDD_15
GPU_VDD_16
GPU_VDD_17
GPU_VDD_18
GPU_VDD_19
GPU_VDD_20



DGND

DGND

DGND

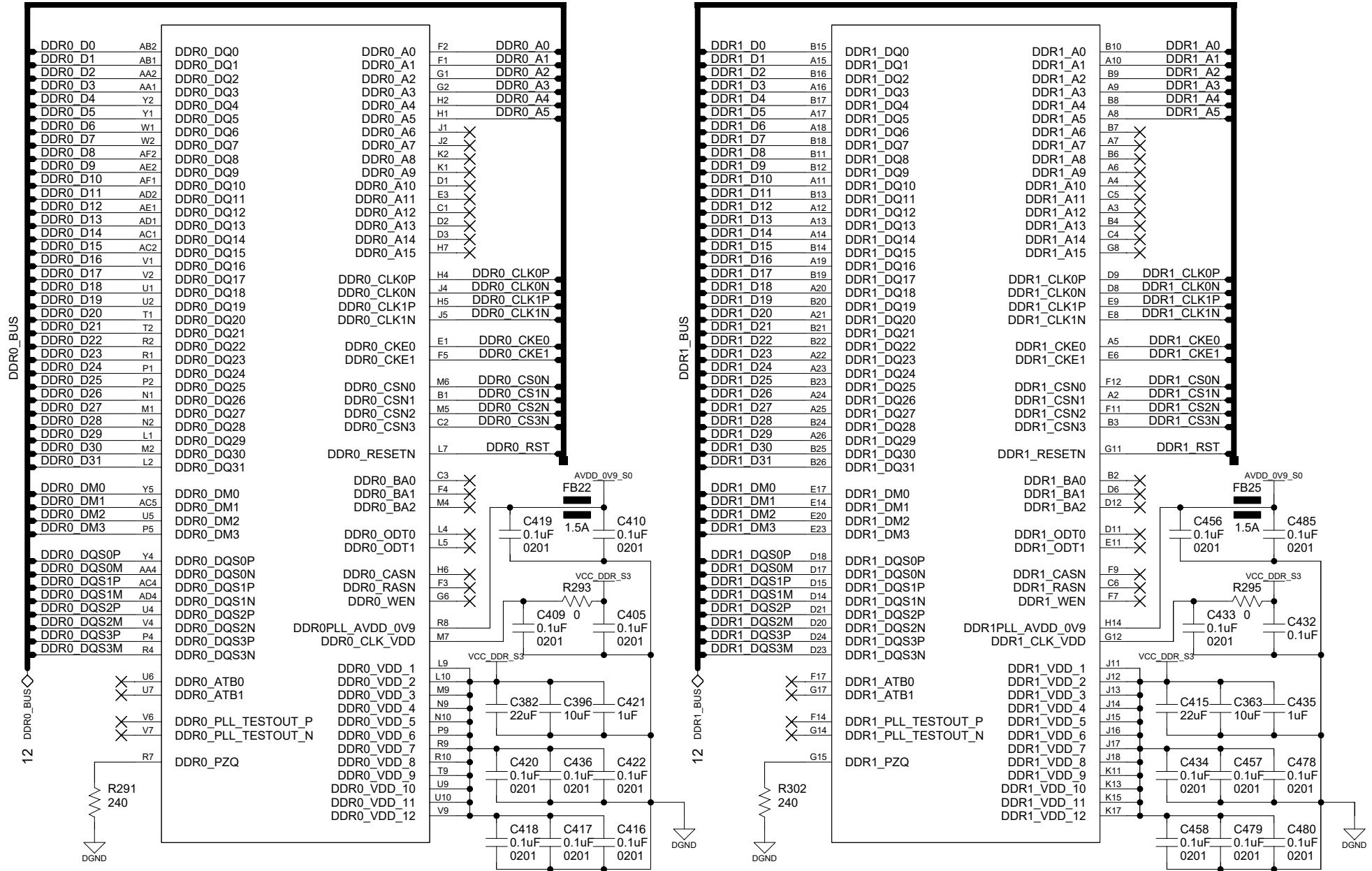
DGND

SIZE	DWG NO	REV
A4	Helios64 NAS	1.2
DRAWN BY D.Sidorovich		SHEET 10 of 37 Variant SJK
CHECKED BY Gauthier Provost Aditya Prayoga		
soc_pwr		05/10/2020

U27
RK3399

LPDDR4 / SoC

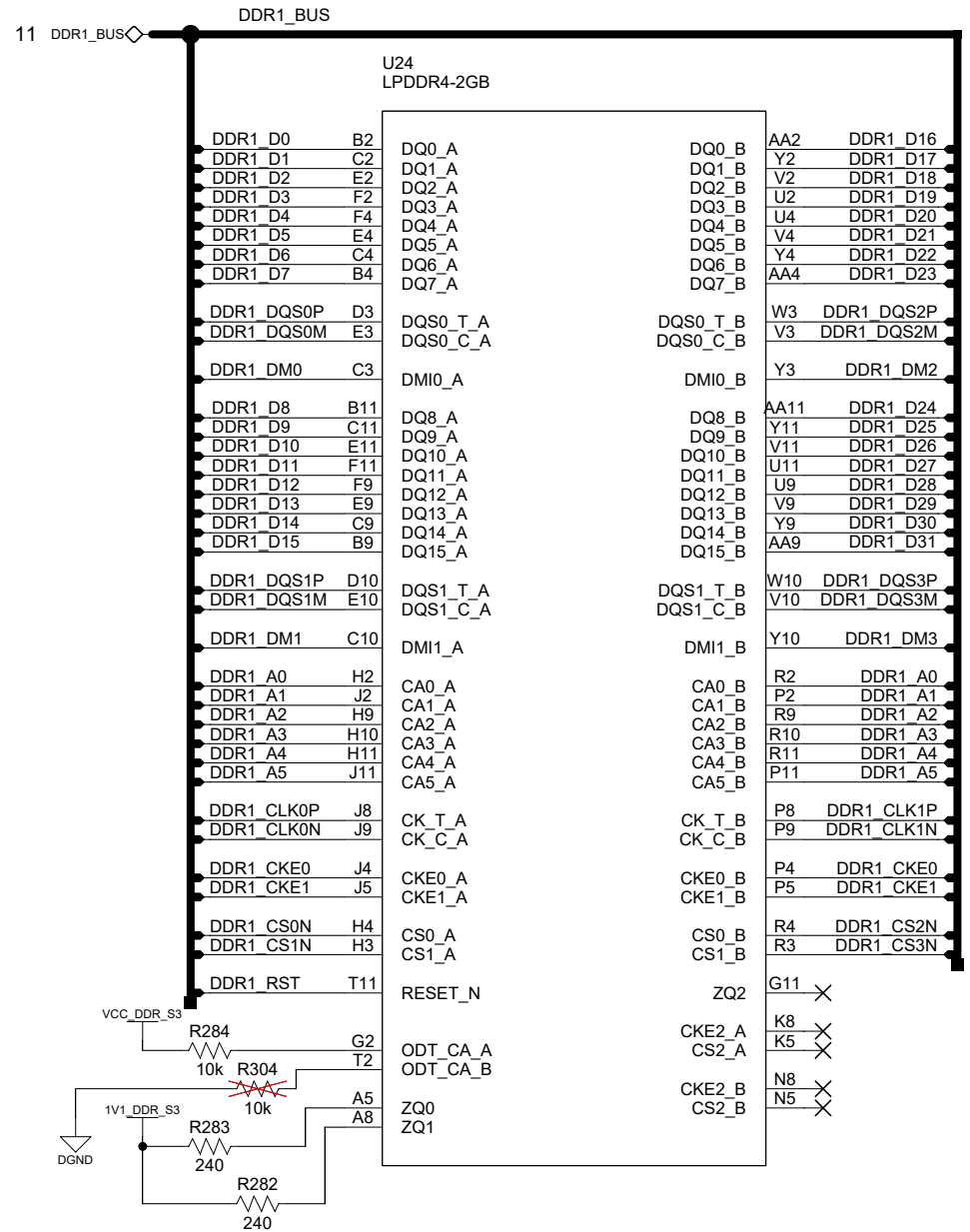
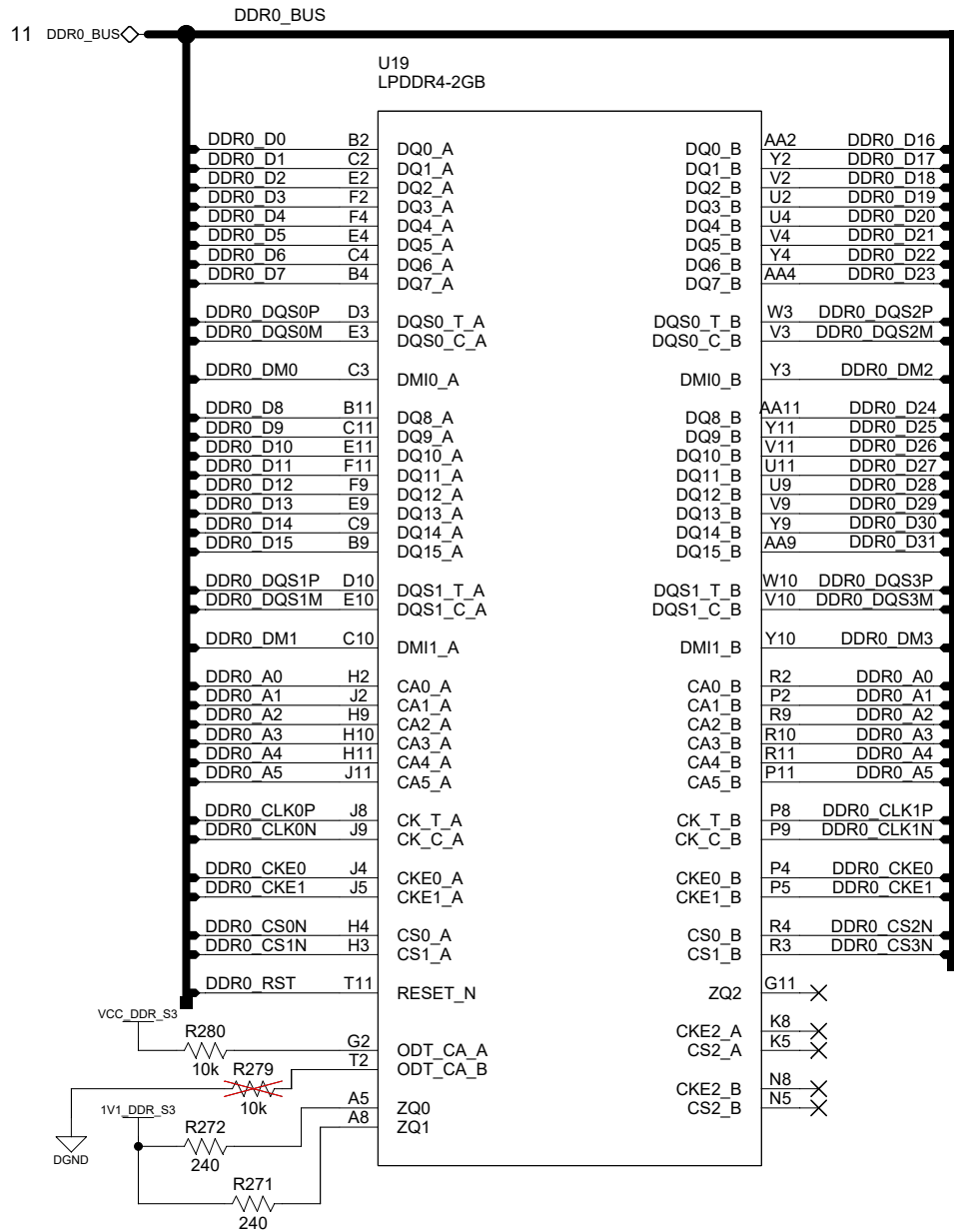
U27
RK3399



SIZE	DWG NO	SHEET of Variant		REV
A4	Helios64 NAS	11	37	1.2
DRAWN BY D.Sidorovich		SHEET of Variant		05/10/2020
CHECKED BY Gauthier Provost Aditya Prayoga		SJK		

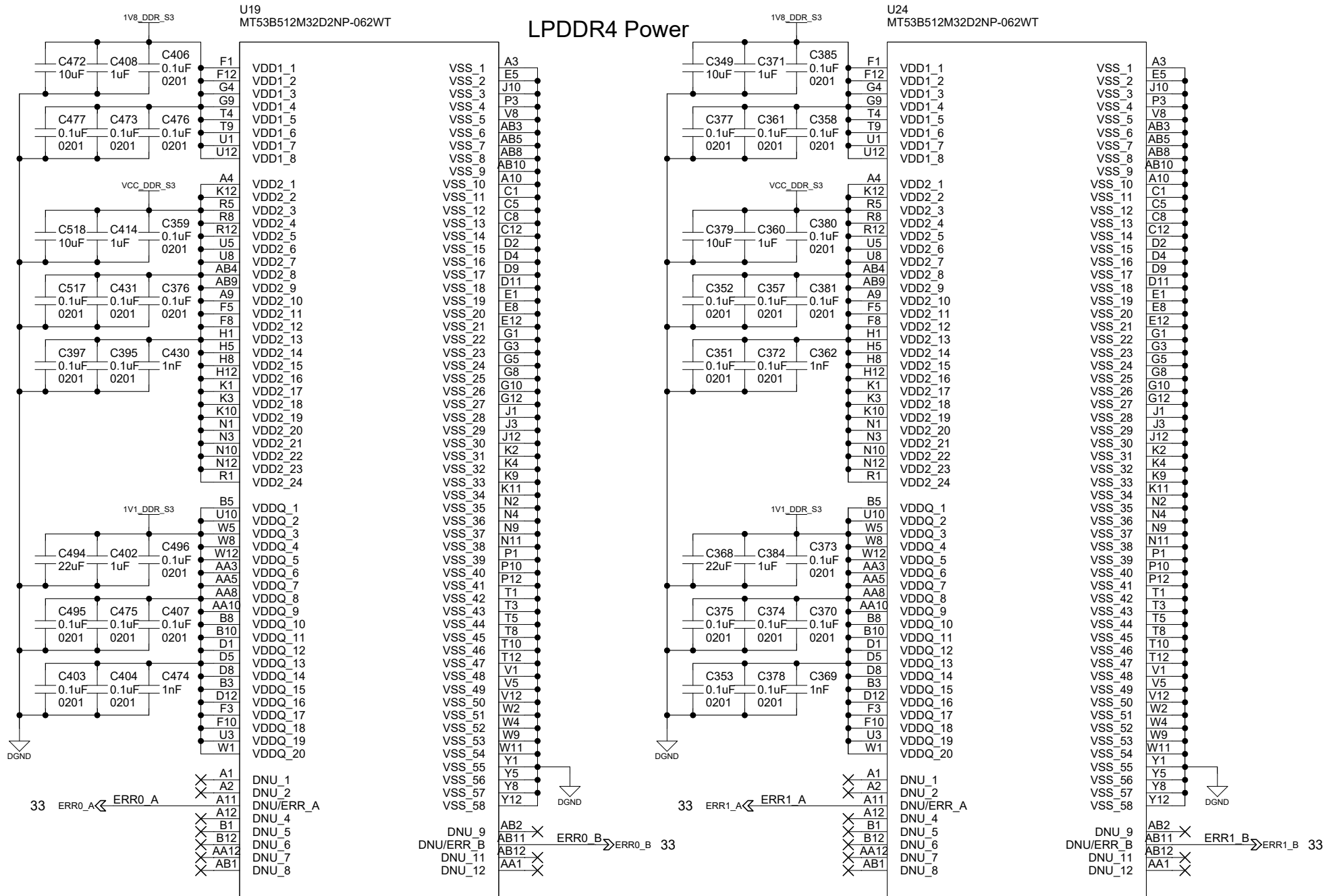
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LPDDR4 2x 2GB



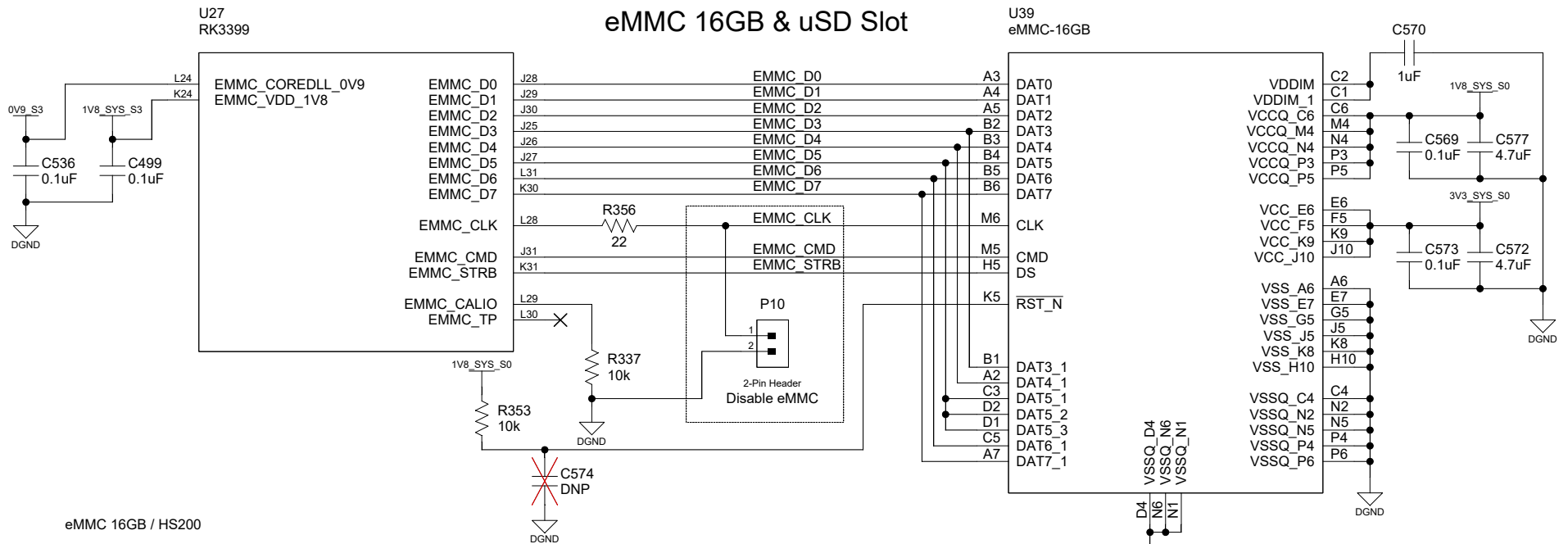
SDRAM - Mobile LPDDR4 Memory ICs
16Gb (512M x 32) 1600MHz - each chip

SIZE	DWG NO	SHEET of Variant		REV
A4	Helios64 NAS	12	37	1.2
DRAWN BY	D.Sidorovich	SHEET of Variant		REV
CHECKED BY	Gauthier Provost Aditya Prayoga	12	37	1.2
		SJK	05/10/2020	

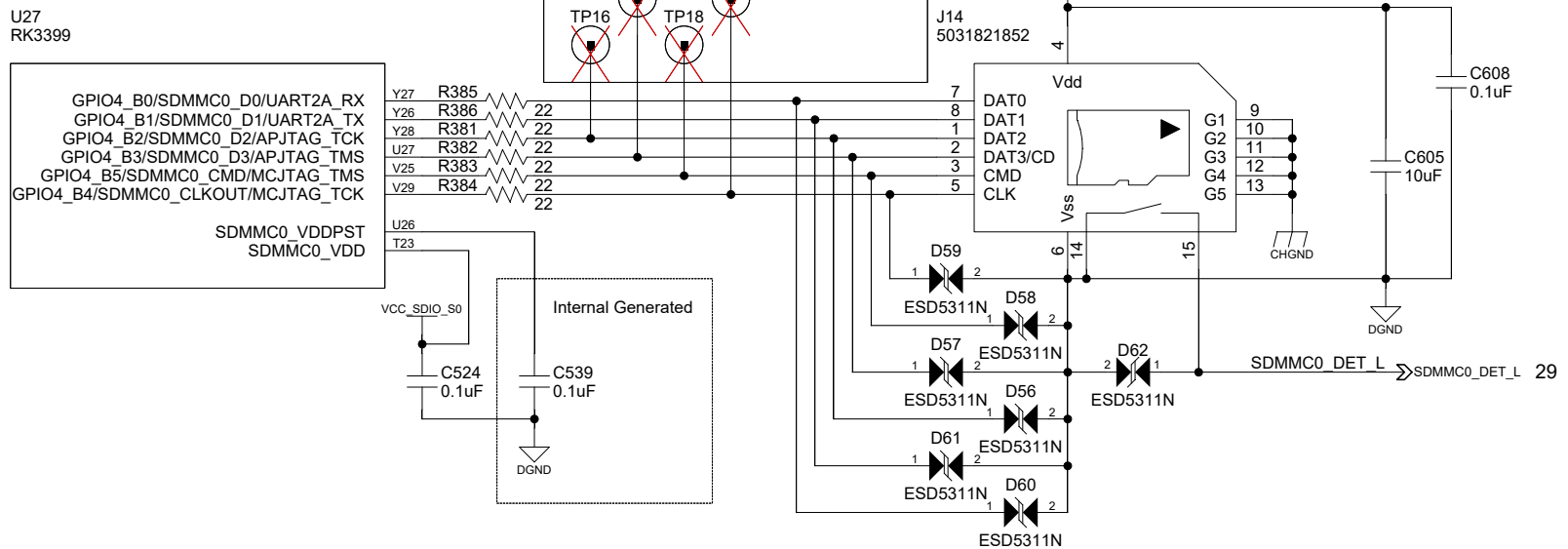


SIZE	DWG NO	SHEET of Variant		REV
A4	Helios64 NAS	13	37	1.2
DRAWN BY D.Sidorovich		SHEET of Variant		
CHECKED BY Gauthier Provost		SJK		
Aditya Prayoga		05/10/2020		

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uSD connector with Card Detection

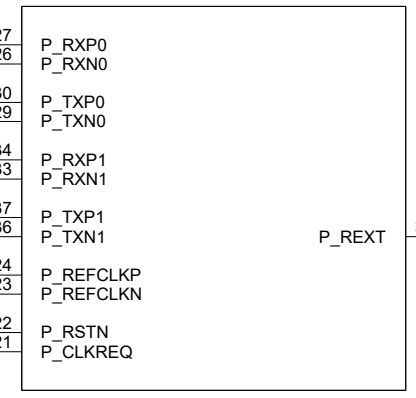
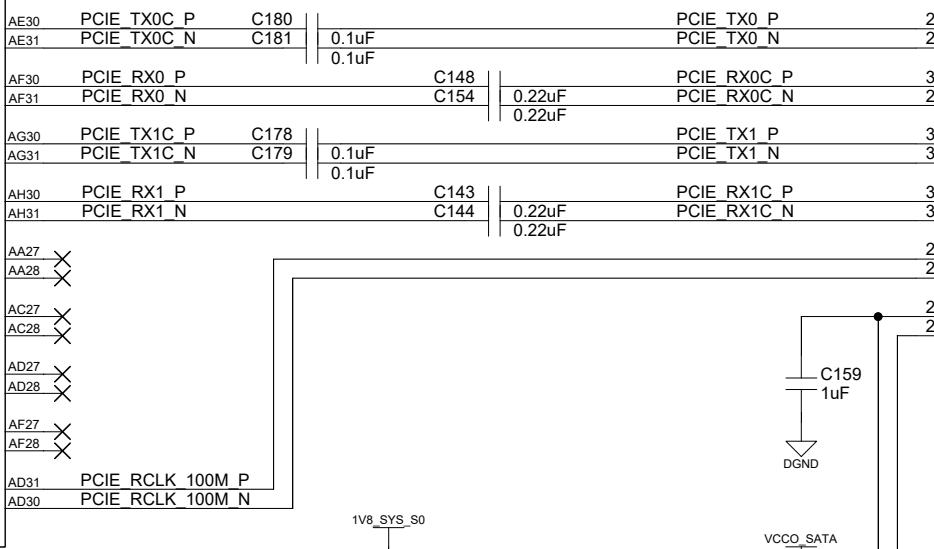
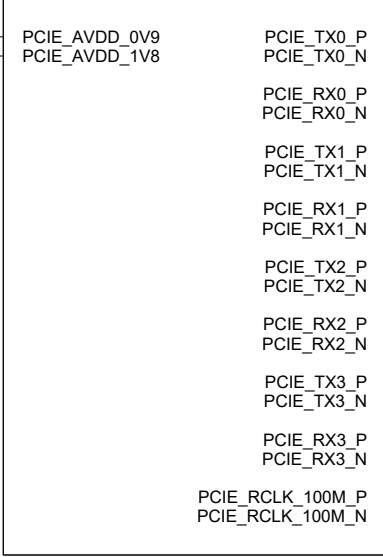
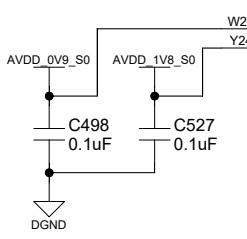


SIZE	DWG NO	REV
A4	Helios64 NAS	emmc_sd 1.2
DRAWN BY	CHECKED BY	SHEET of Variant
D.Sidorovich	Gauthier Provost Aditya Prayoga	14 37 SJK
		05/25/2020

SATA to PCIe Bridge

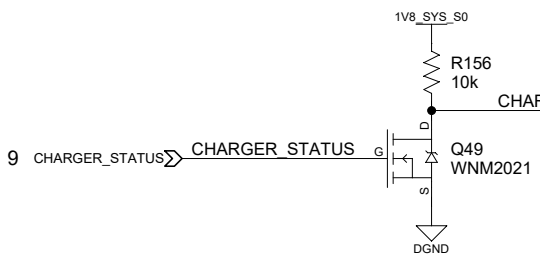
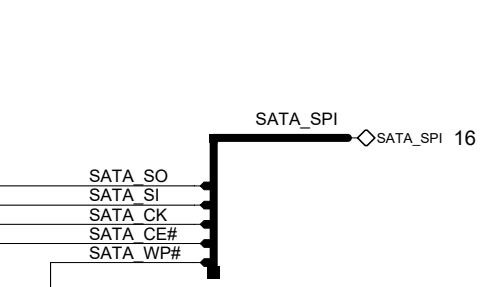
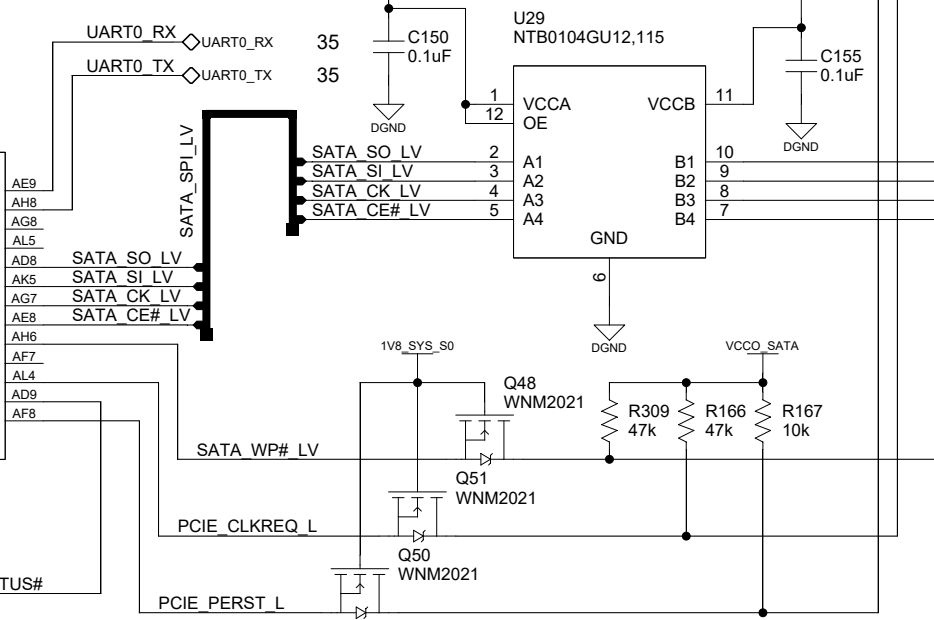
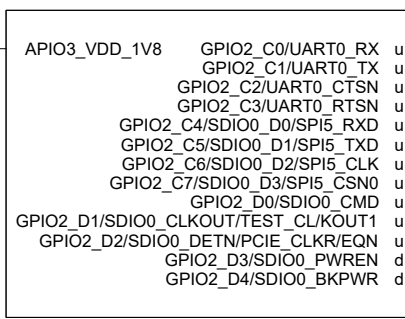
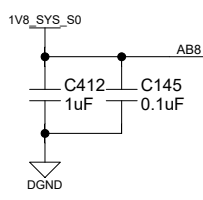
U27
RK3399

U28
JMB585-QH



U27
RK3399

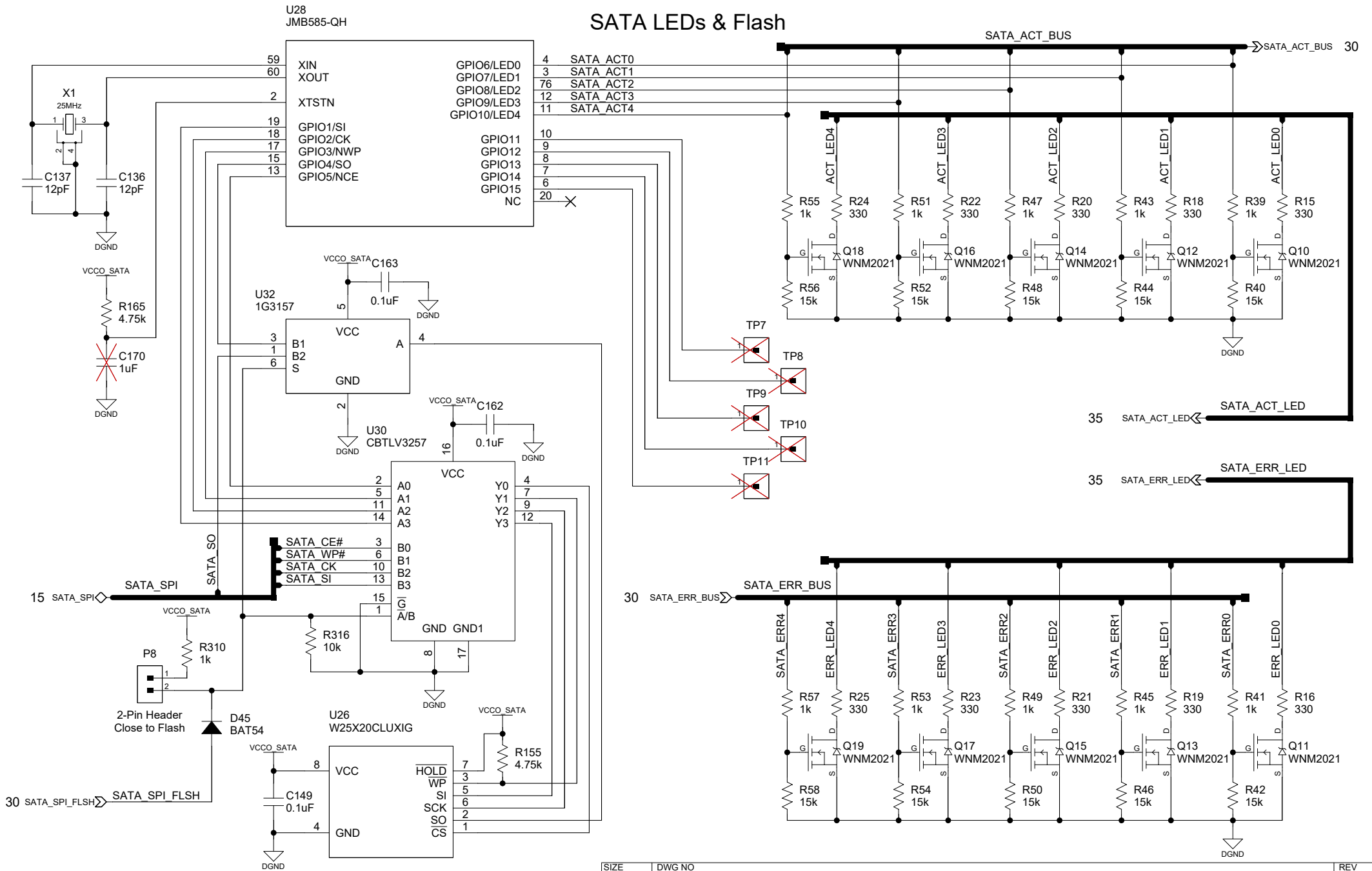
U29
NTB0104GU12,115



CHARGER_STATUS#	Description
0	Complete
1	In progress
Blinking 0.5Hz	Fault/Batt absent

SIZE	DWG NO	SHEET of Variant		REV
A4	Helios64 NAS	15	37	SJK
DRAWN BY D.Sidorovich		SHEET of Variant		05/10/2020
CHECKED BY Gauthier Provost				
Aditya Prayoga				

SATA LEDs & Flash



SIZE	DWG NO	SHEET of Variant		REV
A4	Helios64 NAS	16	37	1.2
DRAWN BY D.Sidorovich		SJK		05/15/2020
CHECKED BY Gauthier Provost Aditya Prayoga				

SATA Connectors

U28
JMB585-QH

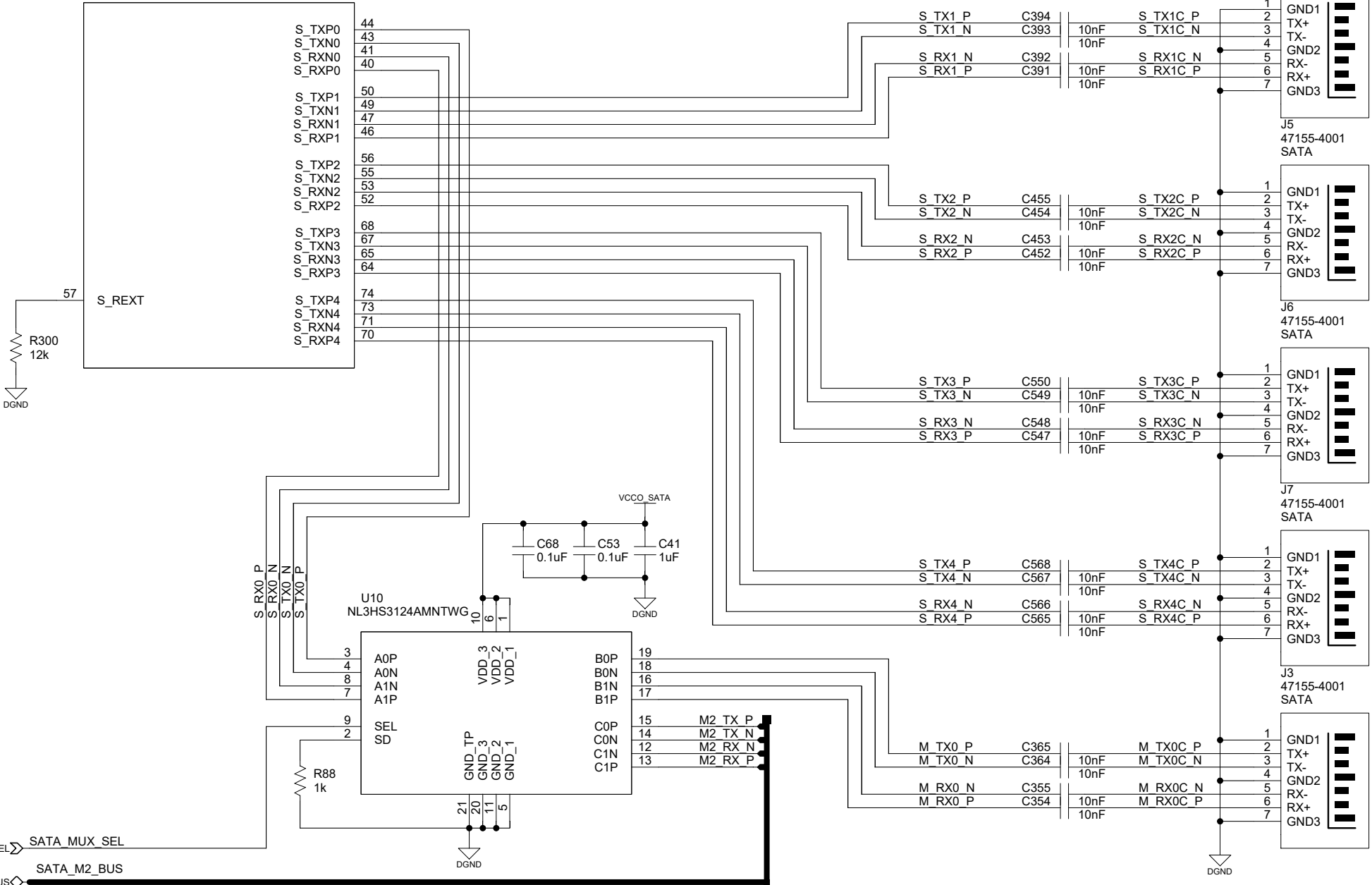
J4
47155-4001
SATA

J5
47155-4001
SATA

J6
47155-4001
SATA

J7
47155-4001
SATA

J3
47155-4001
SATA

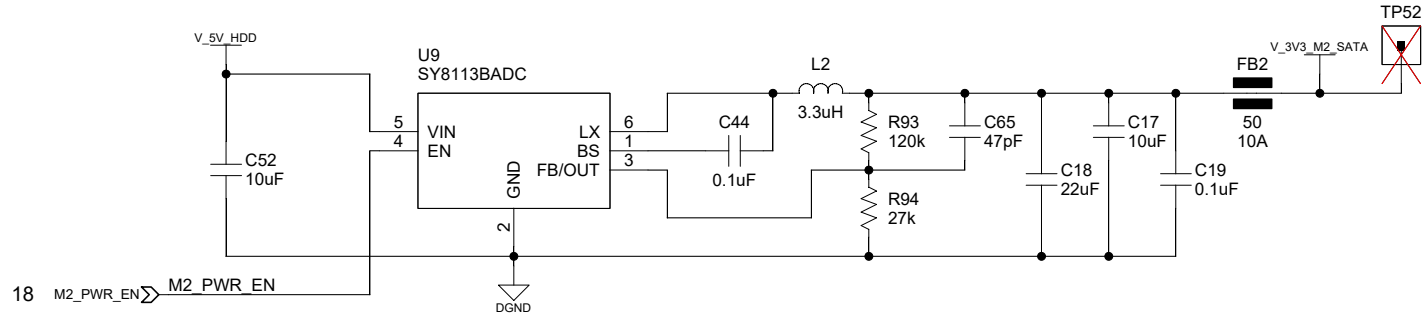


18 SATA_MUX_SEL → SATA_MUX_SEL
18 SATA_M2_BUS → SATA_M2_BUS

SIZE	DWG NO			REV
A4	Helios64 NAS	sata_hdd		1.2
DRAWN BY	D.Sidorovich	SHEET	of	Variant
CHECKED BY	Gauthier Provost Aditya Prayoga	17	37	SJK
		05/11/2020		

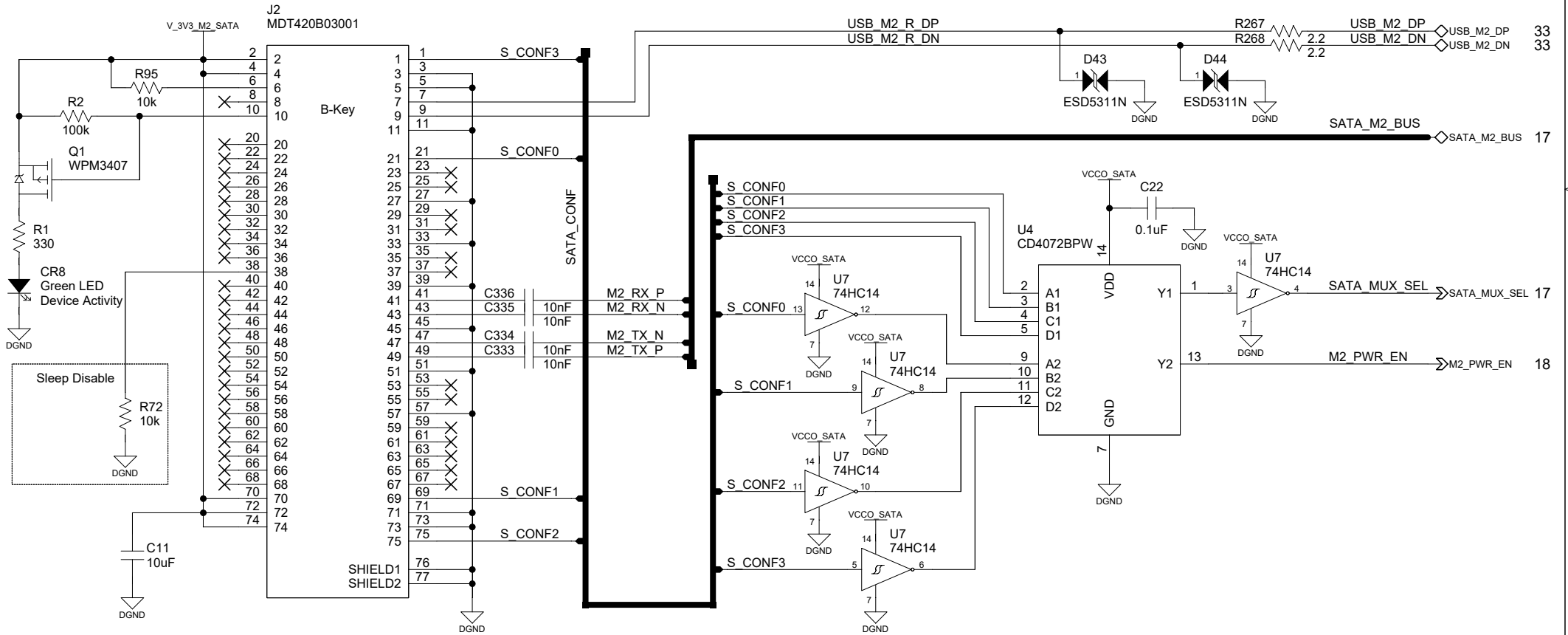
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M.2 SATA



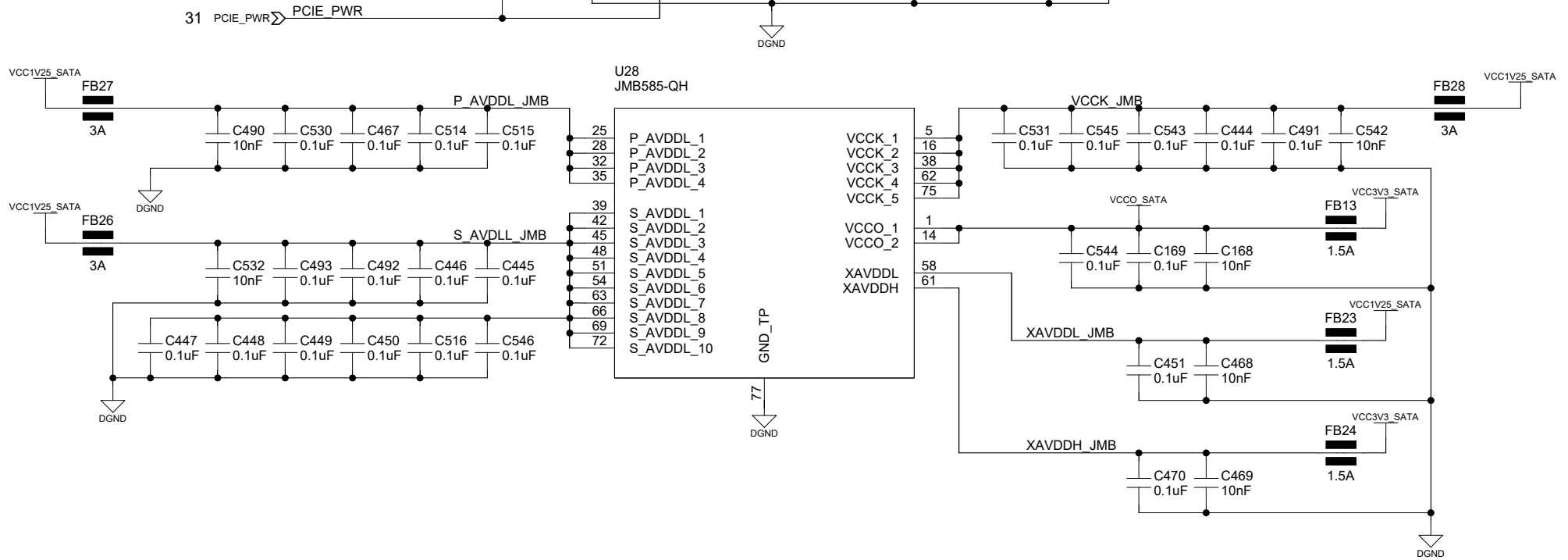
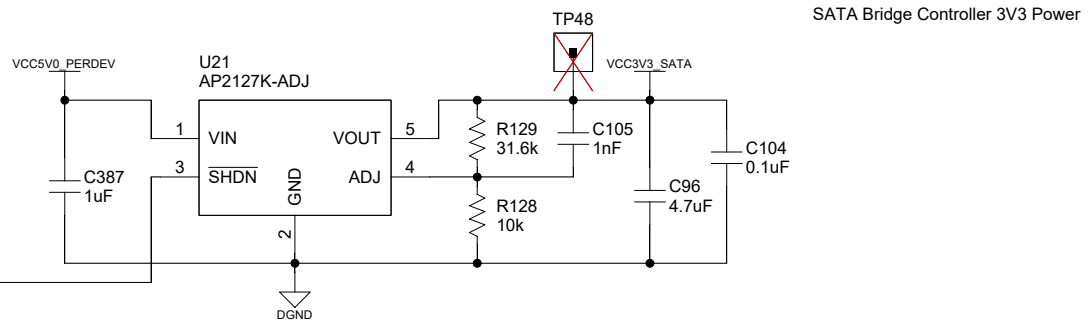
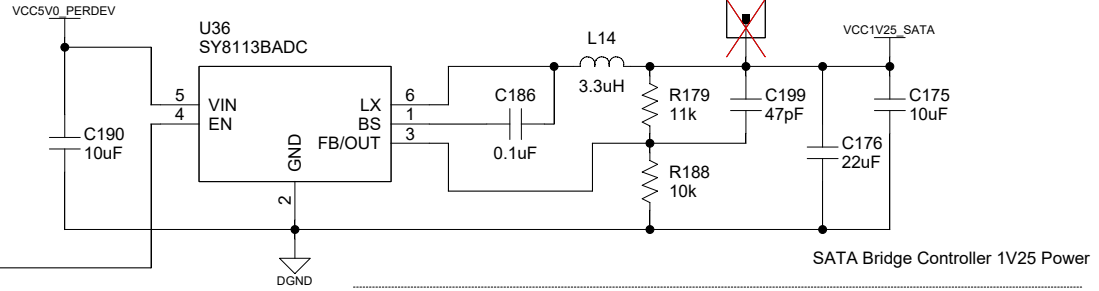
SATA M.2 3V3 Power

M.2 Connector



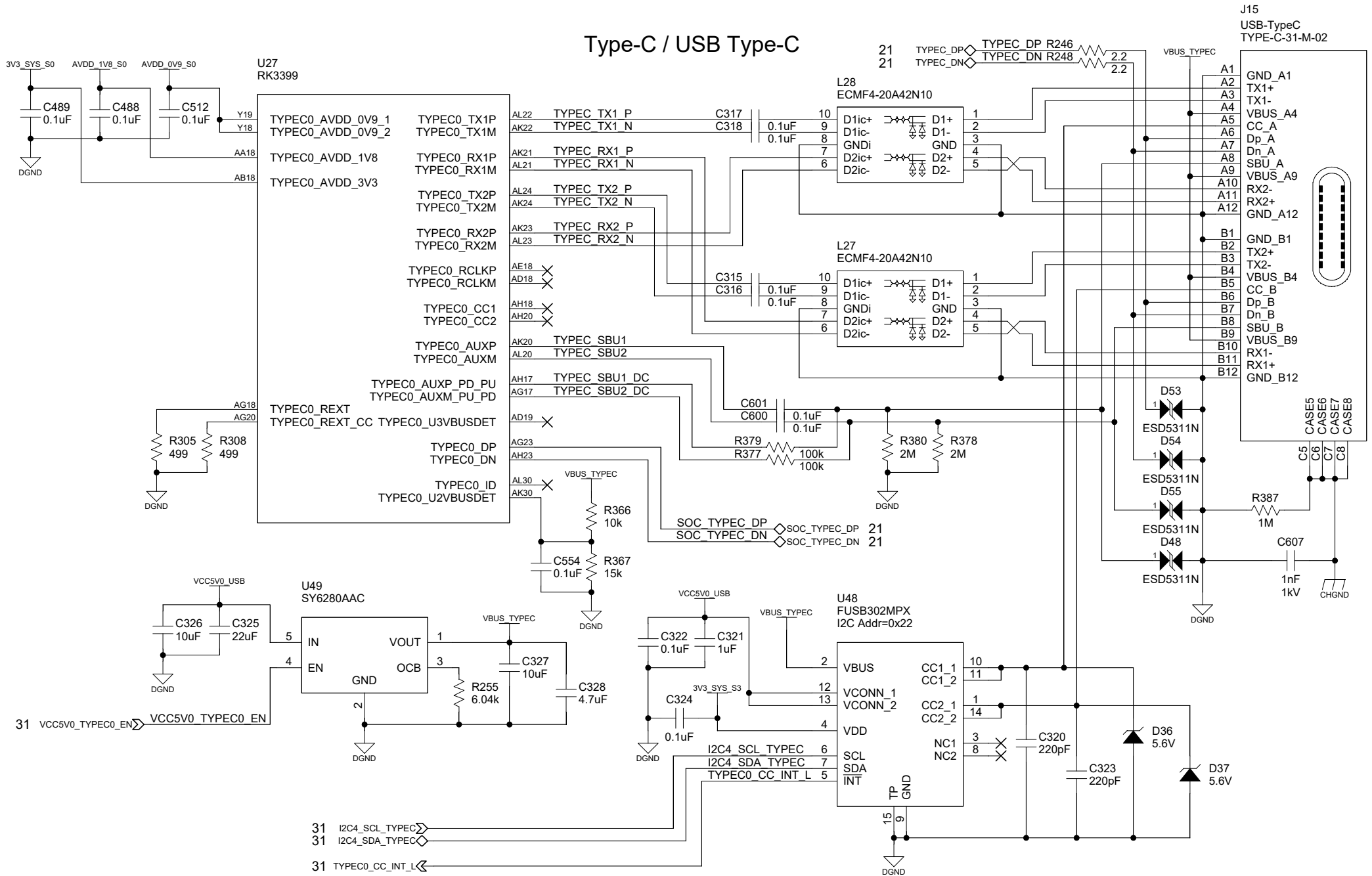
SIZE	DWG NO			REV
A4	Helios64 NAS	sata_m2		1.2
DRAWN BY	D.Sidorovich	SHEET	of	Variant
CHECKED BY	Gauthier Provost Aditya Prayoga	18	37	SJK
		05/10/2020		

SATA Bridge Power



SIZE	DWG NO	SHEET of Variant		REV
A4	Helios64 NAS	19	37	1.2
DRAWN BY D.Sidorovich		SHEET of Variant		
CHECKED BY Gauthier Provost		SJK		
Aditya Prayoga		05/10/2020		

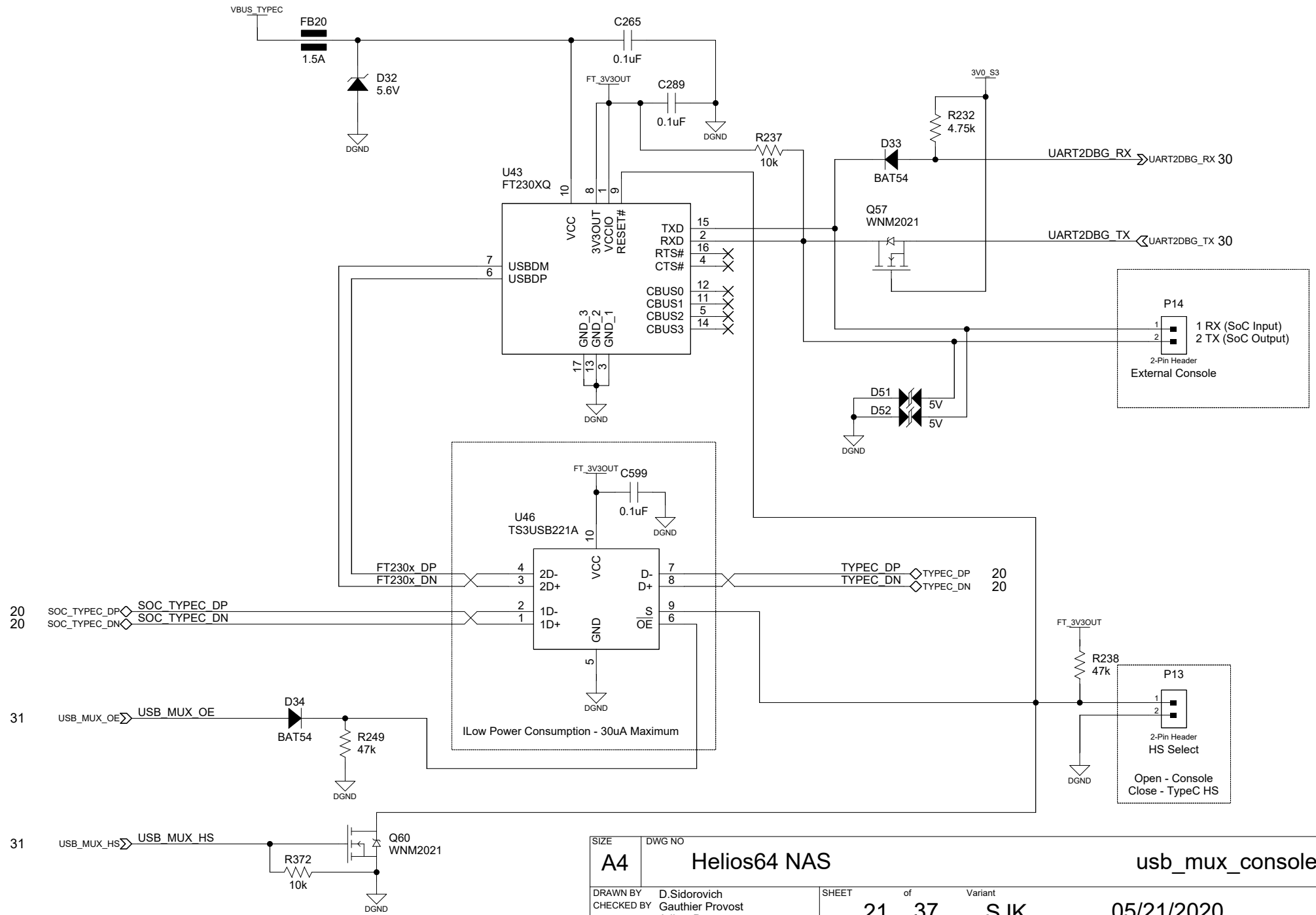
Type-C / USB Type-C



SIZE	DWG NO	SHEET of Variant		REV
A4	Helios64 NAS	20	37	1.2
DRAWN BY	D.Sidorovich	CHECKED BY	Gauthier Provost Aditya Prayoga	SJK
DATE			06/01/2020	typec_port

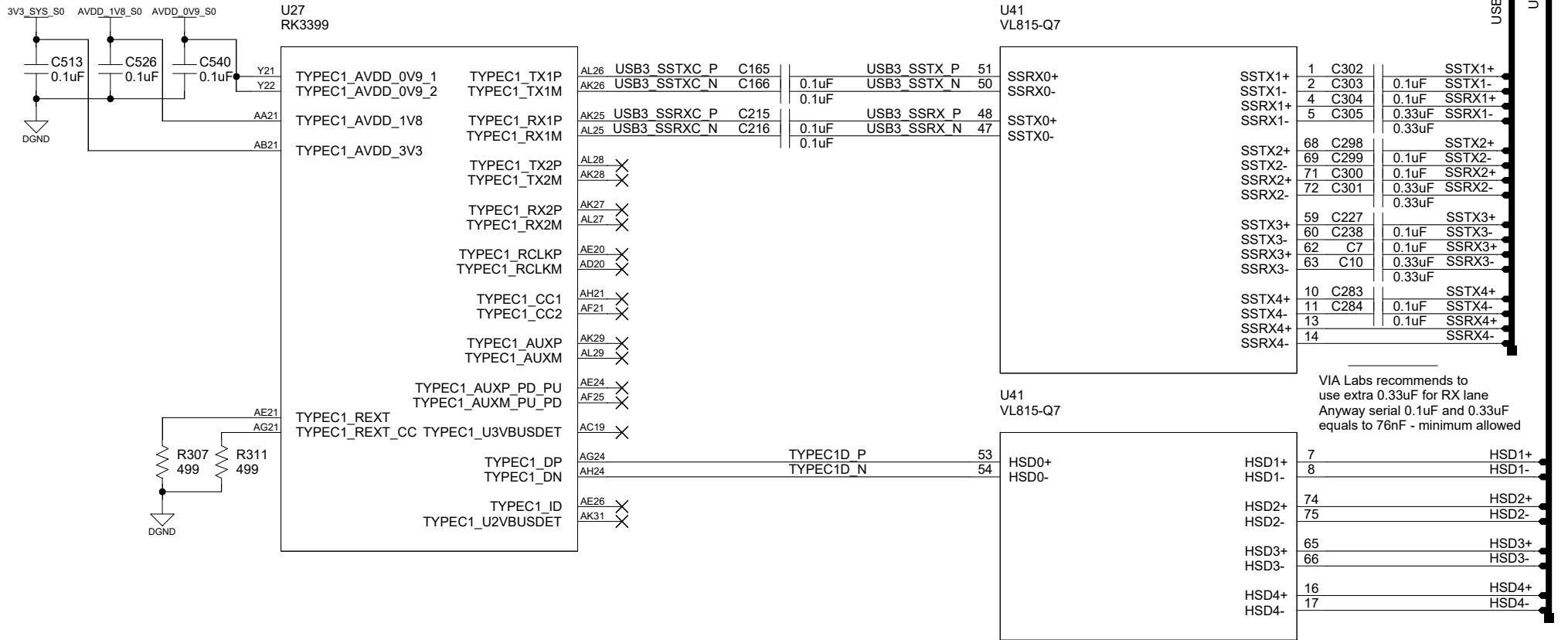
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USB / UART Console & Type-C HS MUX 2:1



SIZE	DWG NO	SHEET of Variant		REV
A4	Helios64 NAS	21	37	SJK
DRAWN BY D.Sidorovich		SHEET of Variant		REV
CHECKED BY Gauthier Provost		21 37 SJK		1.2
Aditya Prayoga		05/21/2020		usb_mux_console

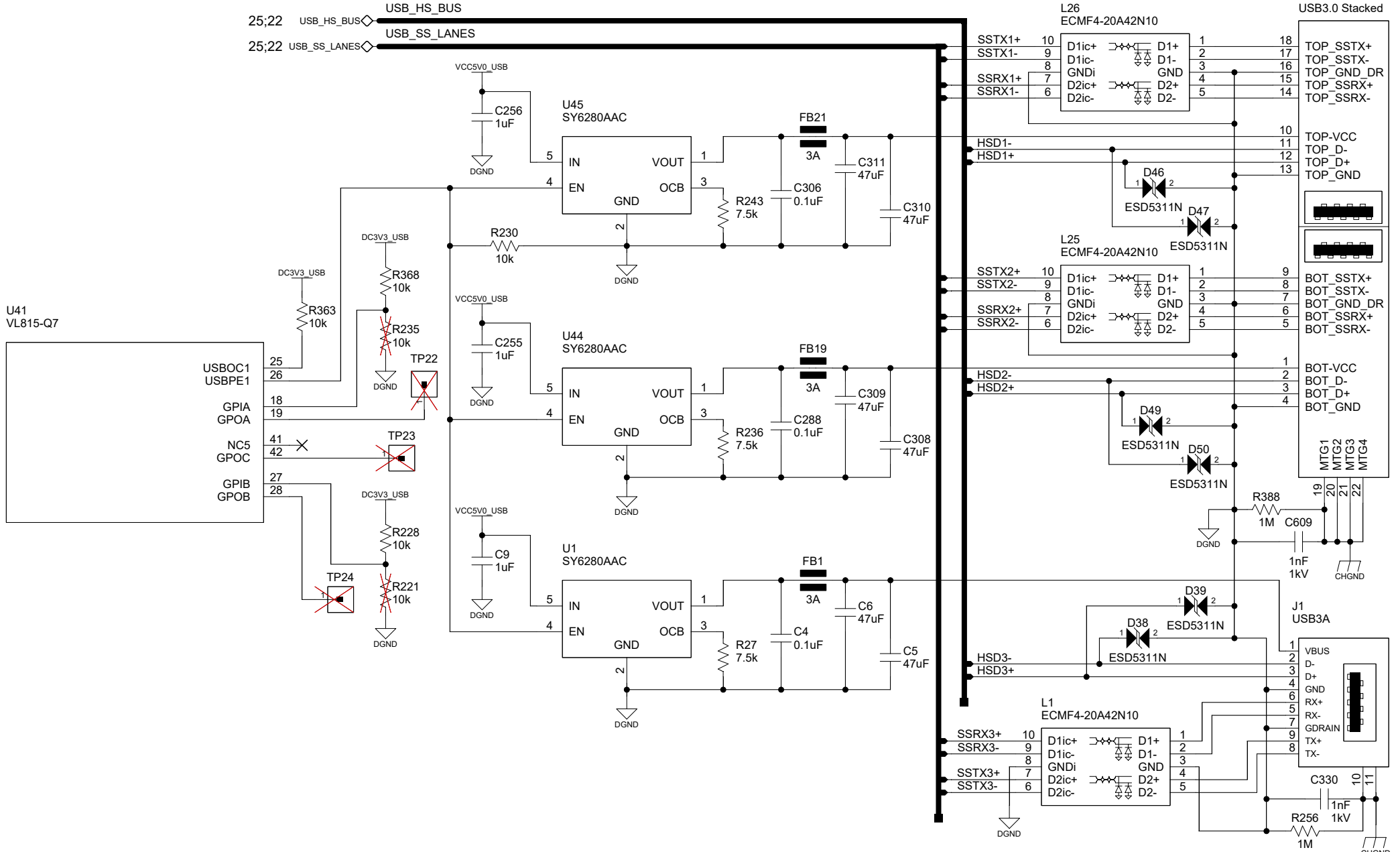
Type-C / USB3 HUB



SIZE	DWG NO	SHEET of Variant		REV
A4	Helios64 NAS	22	37	1.2
DRAWN BY D.Sidorovich		SHEET of Variant		REV
CHECKED BY Gauthier Provost		22	37	1.2
Aditya Prayoga		SJK	05/10/2020	

USB3 Stacked Ports

J13
048406003
USB3.0 Stacked



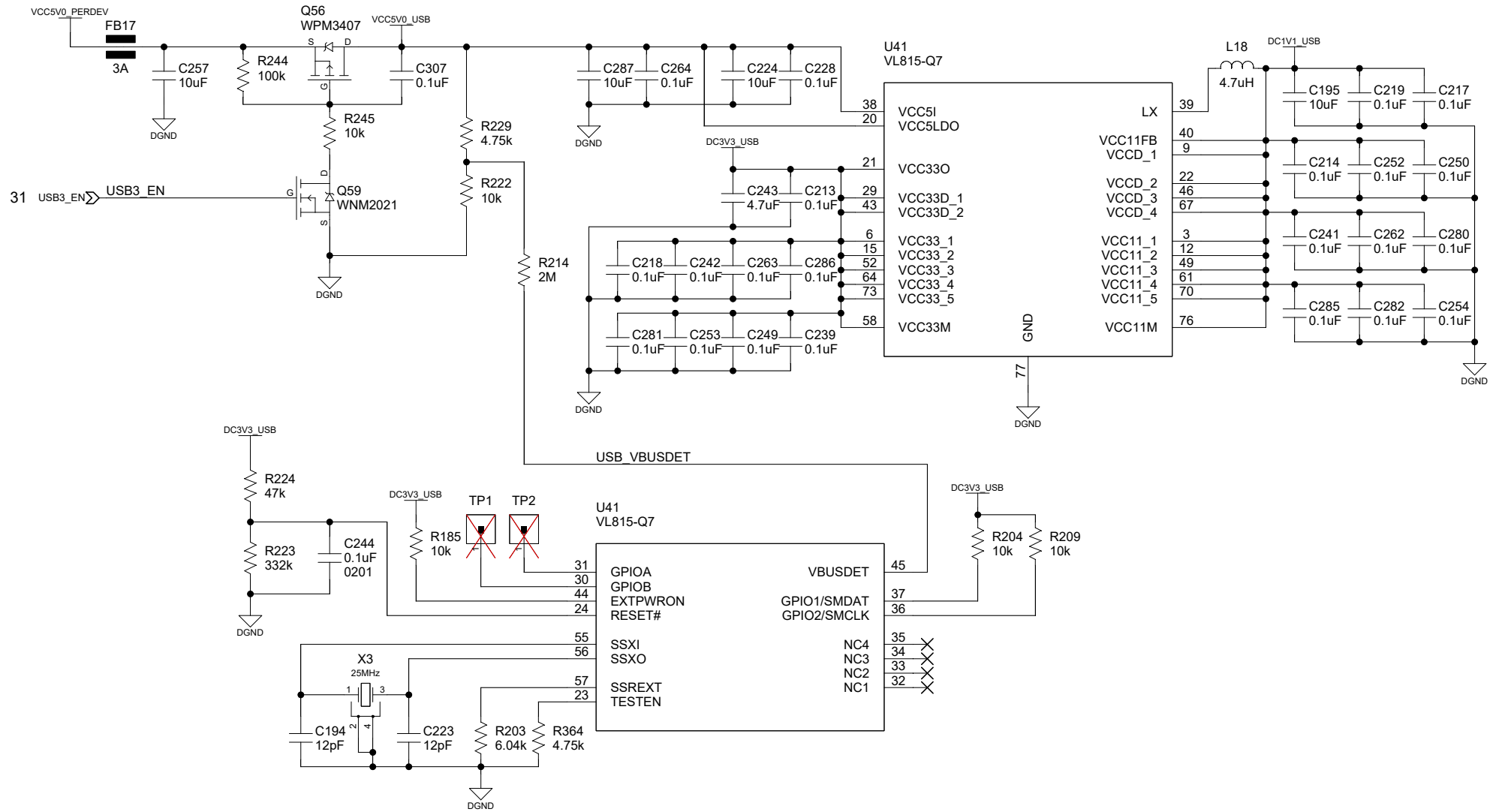
U41
VL815-Q7

USBOC1
USBPE1
GPIA
GPOA
NC5
GPOC
GPIB
GPOB

SIZE	DWG NO	SHEET of Variant		REV
A4	Helios64 NAS	23	37	SJK
DRAWN BY D.Sidorovich		CHECKED BY Gauthier Provost		06/01/2020
		Aditya Prayoga		
usb3_hub_ports			1.2	

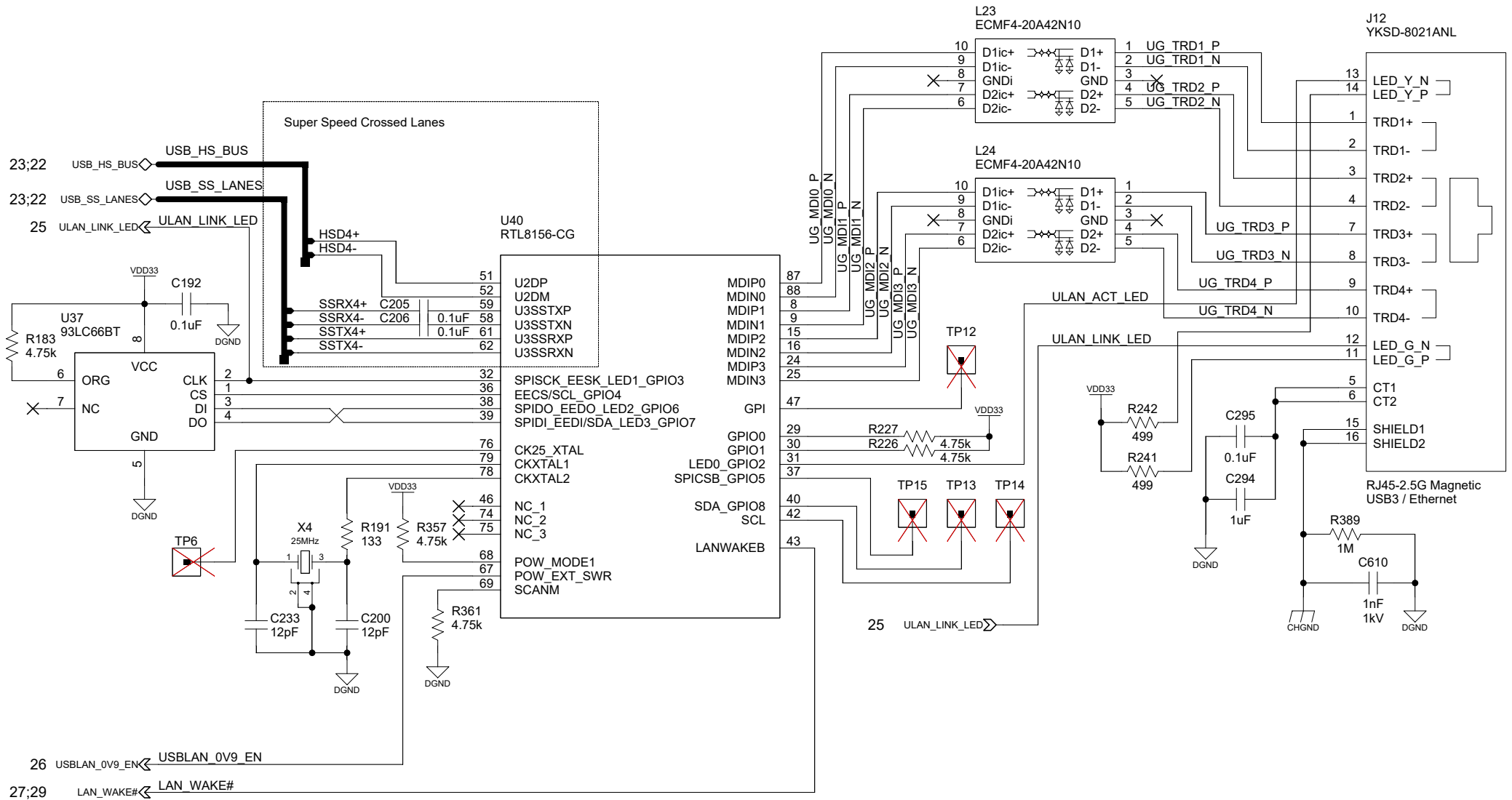
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USB3 HUB Power



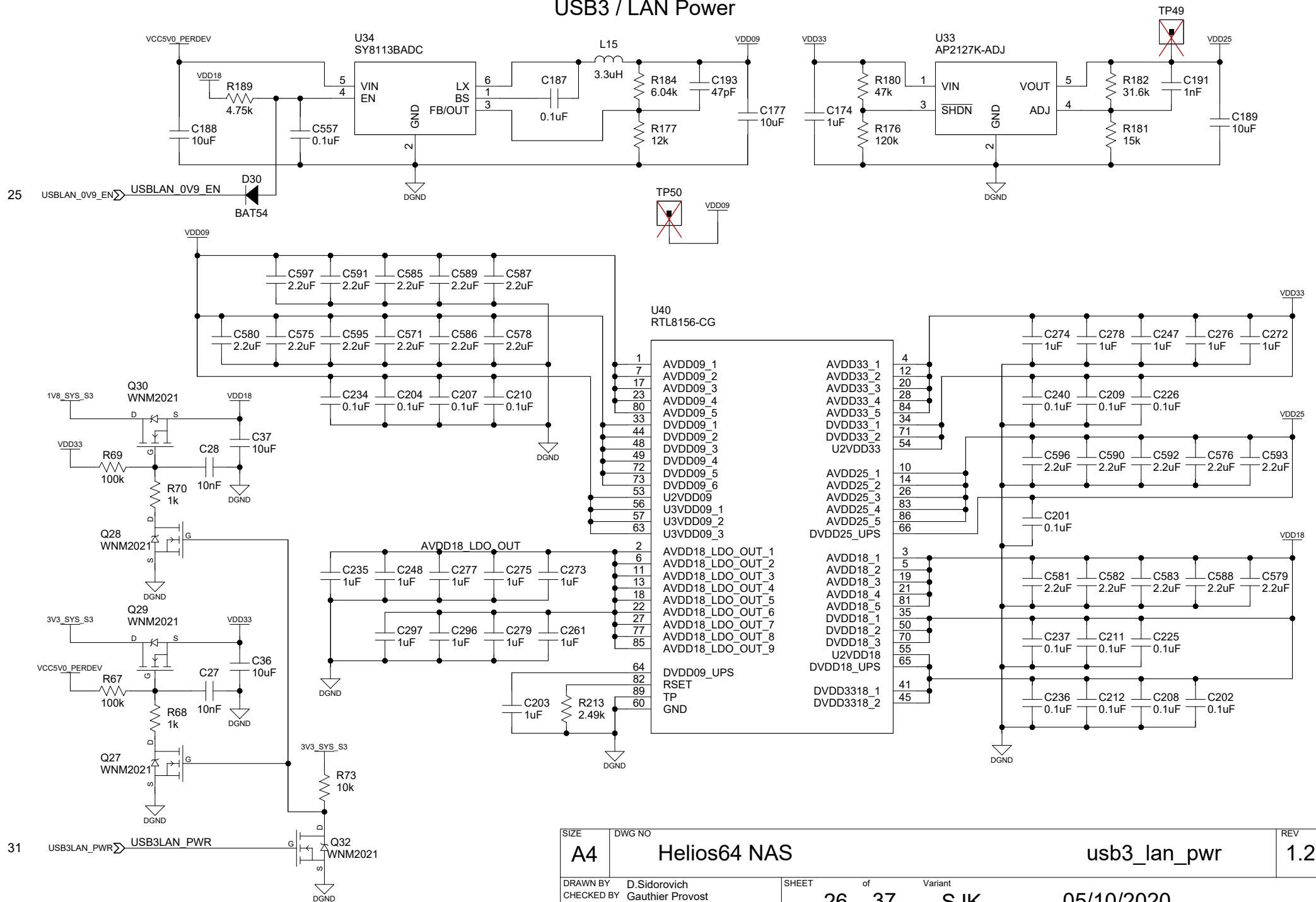
SIZE	DWG NO			REV
A4	Helios64 NAS	usb3_hub_pwr_led		1.2
DRAWN BY	D.Sidorovich	SHEET	of	Variant
CHECKED BY	Gauthier Provost Aditya Prayoga	24	37	SJK
				06/01/2020

USB3 to 1.0/2.5 GbE LAN



SIZE	DWG NO	SHEET of Variant		REV
A4	Helios64 NAS	25	37	1.2
DRAWN BY D.Sidorovich		SHEET of Variant		REV
CHECKED BY Gauthier Provost		25	37	1.2
Aditya Prayoga		SJK	06/01/2020	

USB3 / LAN Power



25 USBLAN_0V9_EN

31 USB3LAN_PWR

SIZE	DWG NO	SHEET of Variant		REV
A4	Helios64 NAS	26	37	1.2
DRAWN BY D.Sidorovich		SHEET of Variant		
CHECKED BY Gauthier Provost Aditya Prayoga		26	37	1.2
			SJK	05/10/2020
				usb3_lan_pwr

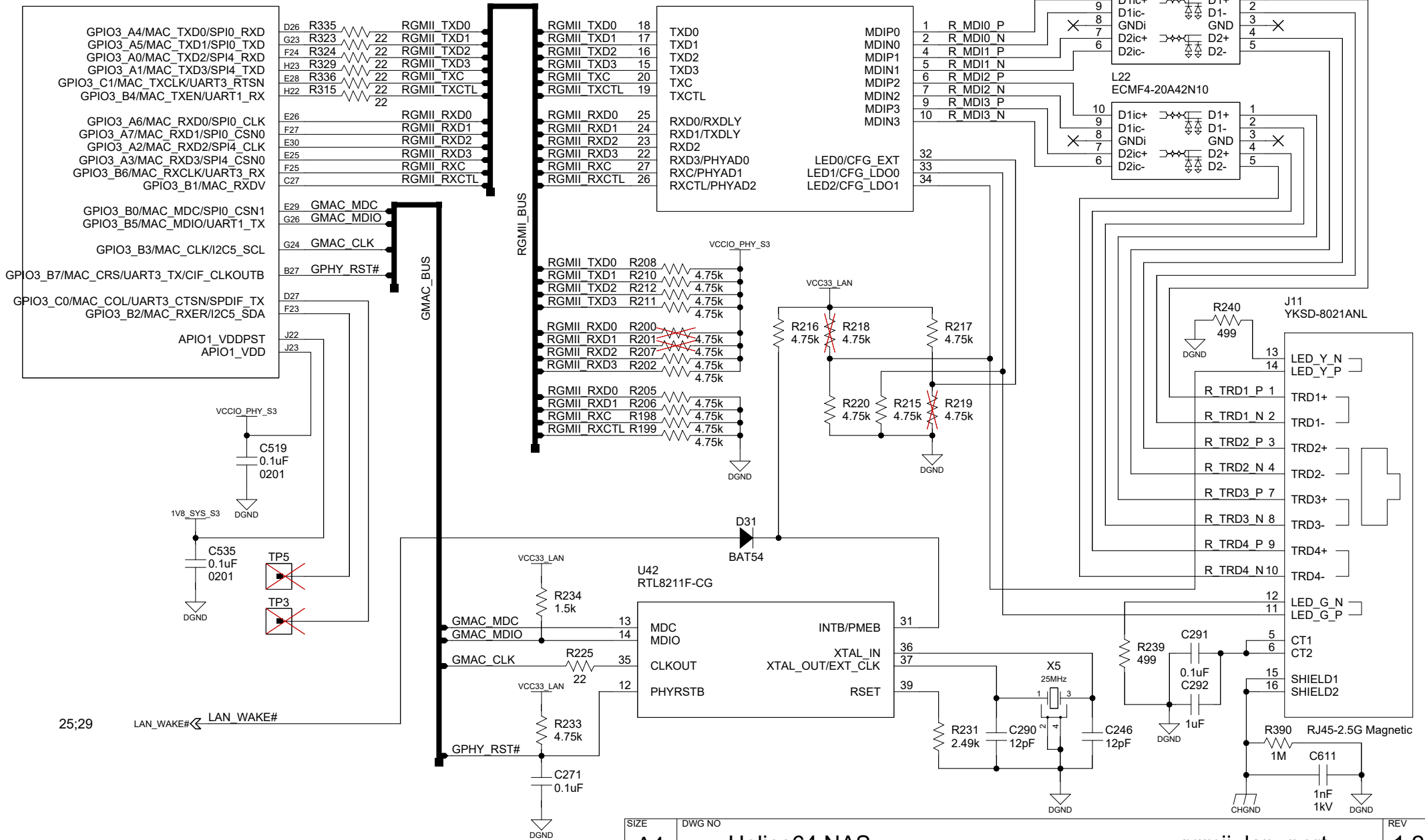
1Gbe Port

U27
RK3399

U42
RTL8211F-CG

L21
ECMF4-20A42N10

L22
ECMF4-20A42N10



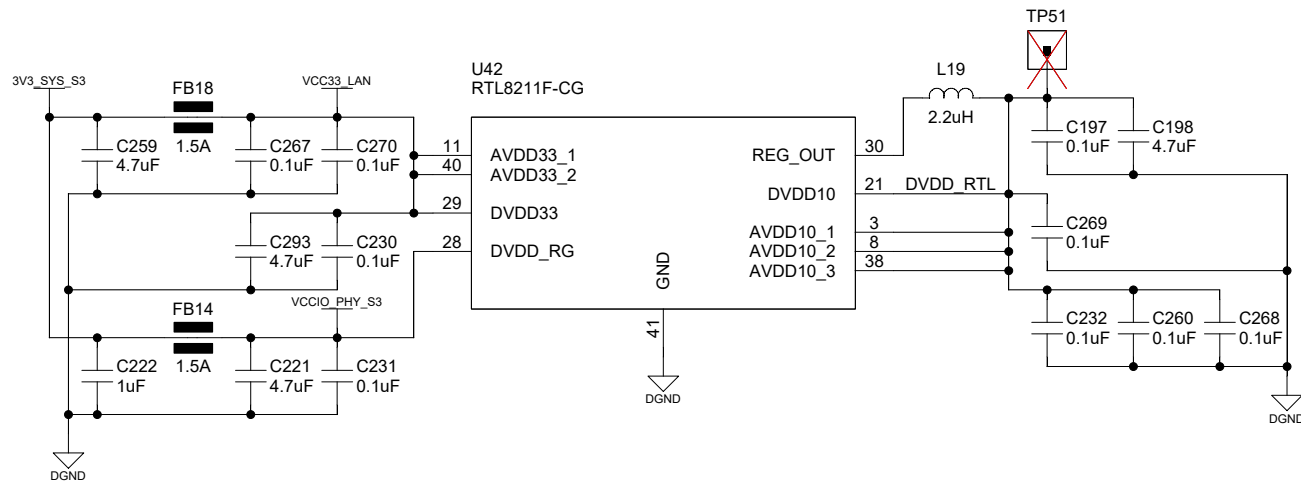
25;29

LAN_WAKE# ← LAN_WAKE#

SIZE	DWG NO	SHEET of Variant		REV
A4	Helios64 NAS	27	37	1.2
DRAWN BY	CHECKED BY	SJK		06/01/2020
D.Sidorovich	Gauthier Provost Aditya Prayoga			

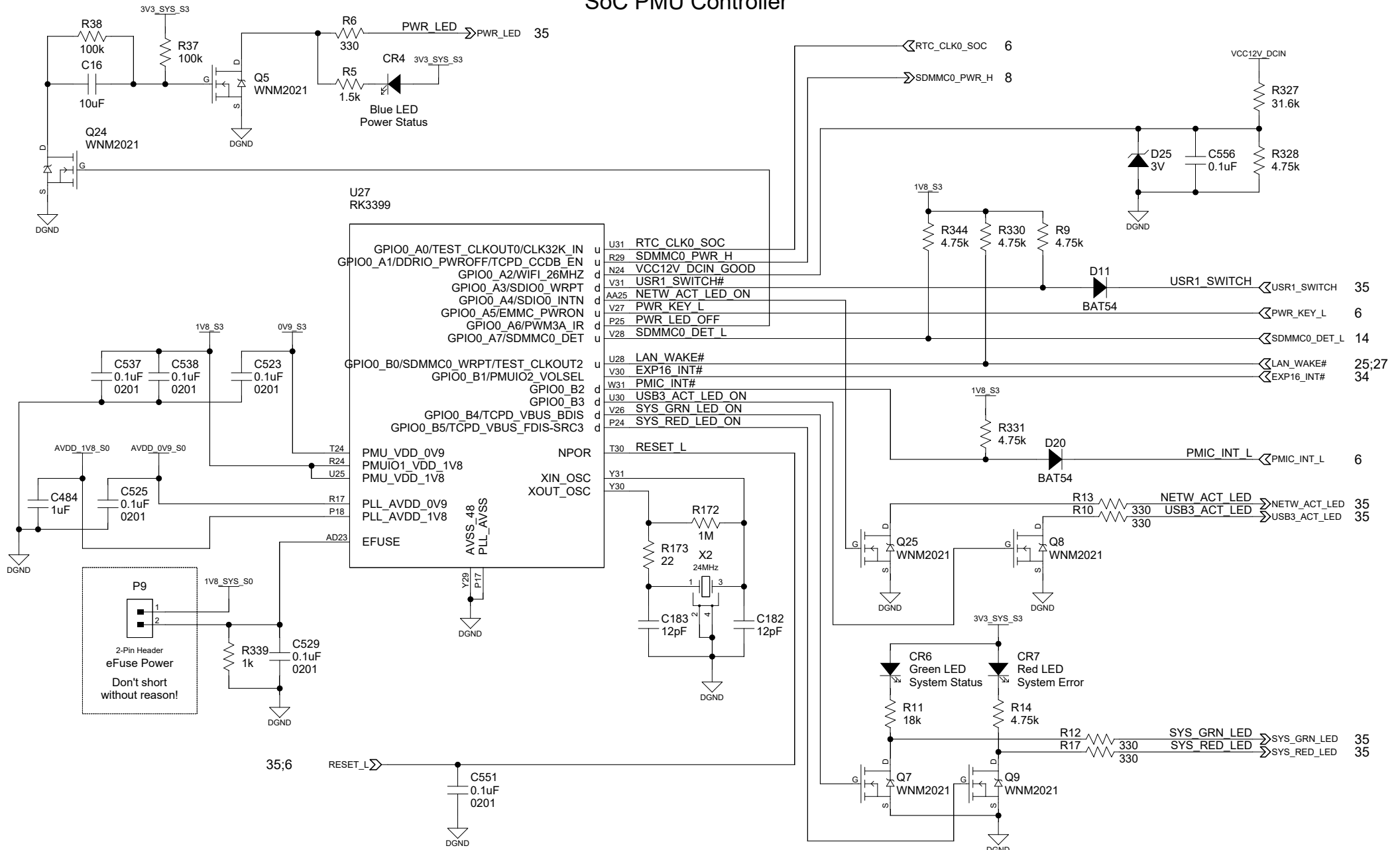
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1Gbe Phy Power



SIZE	DWG NO	SHEET of Variant		REV
A4	Helios64 NAS	28	37	1.2
DRAWN BY D.Sidorovich		SHEET of Variant		rgmii_lan_pwr
CHECKED BY Gauthier Provost		28	37	05/07/2020
Aditya Prayoga		SJK		

SoC PMU Controller

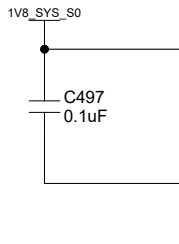
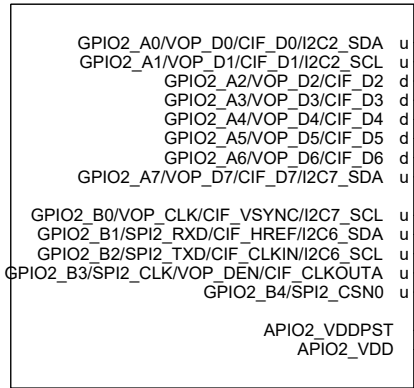


SIZE	DWG NO			REV
A4	Helios64 NAS	soc_pmuc		1.2
DRAWN BY	D.Sidorovich	SHEET	of	Variant
CHECKED BY	Gauthier Provost Aditya Prayoga	29	37	SJK
				05/16/2020

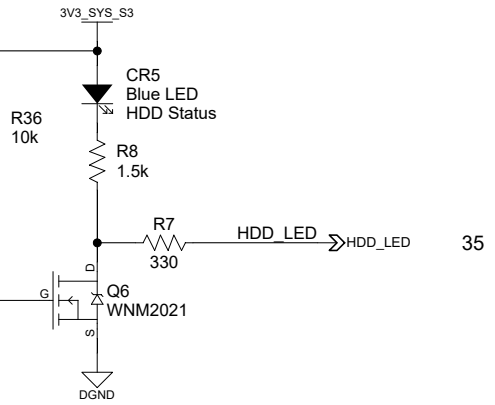
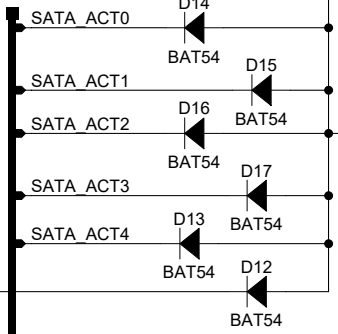
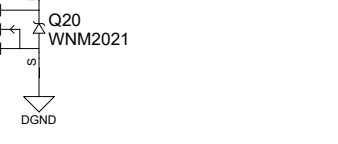
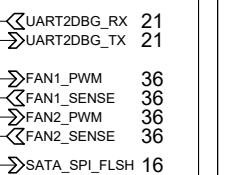
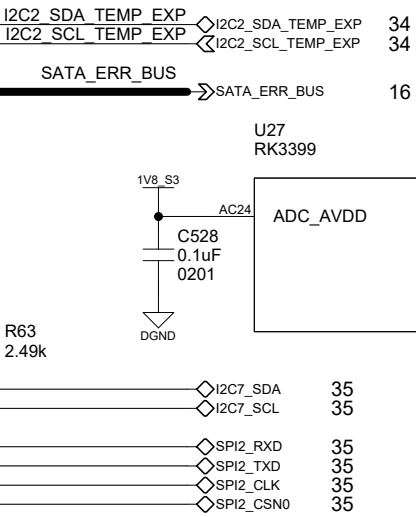
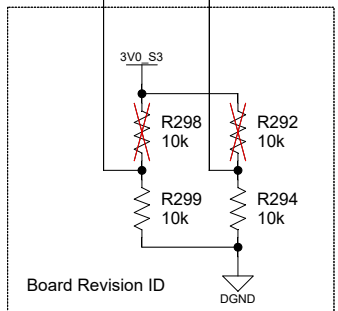
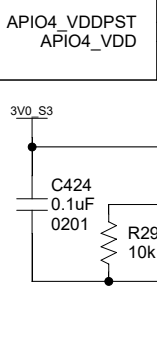
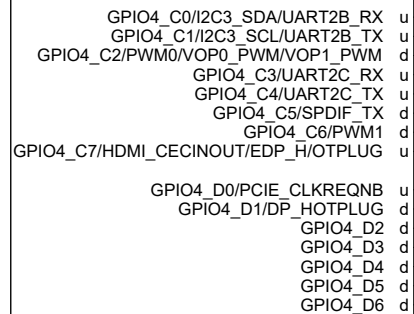
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SoC Recovery / Peripheral Buses / HDD LED / Board ID

U27
RK3399



U27
RK3399



VCC_STBY

C141 0.1uF

DGND

Input, V	Output, V
10.8	1.18
12.0	1.31
13.2	1.45

1.8V

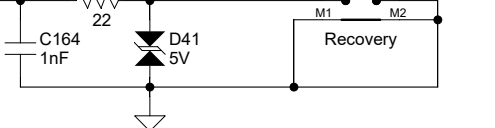
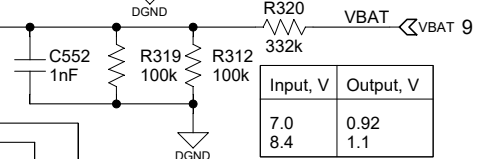
C552 1nF

R319 100k

R312 100k

DGND

Input, V	Output, V
7.0	0.92
8.4	1.1



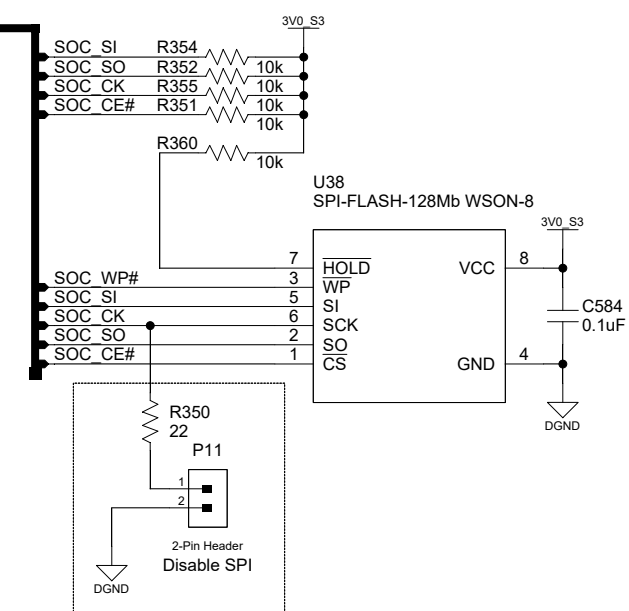
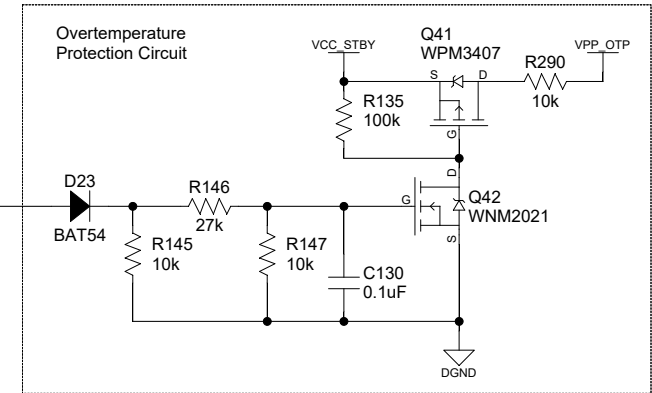
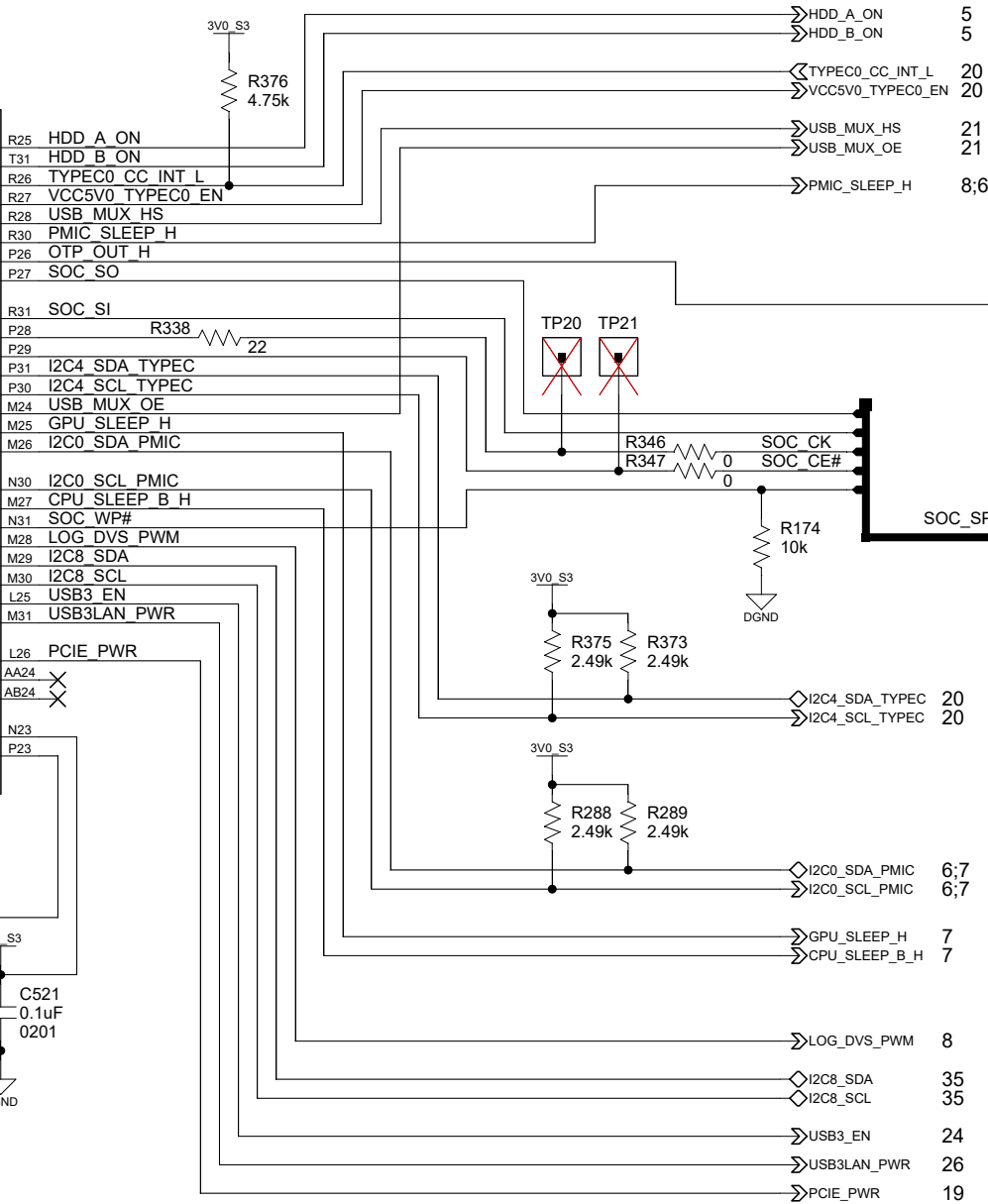
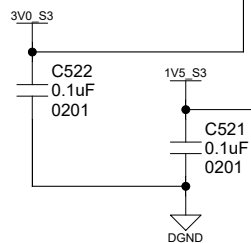
SIZE A4	DWG NO Helios64 NAS	REV 1.2
DRAWN BY D.Sidorovich		SHEET 30 of 37 Variant
CHECKED BY Gauthier Provost Aditya Prayoga		SJK
		05/16/2020

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Power Control / Over Temperature Protection / SoC SPI

U27
RK3399

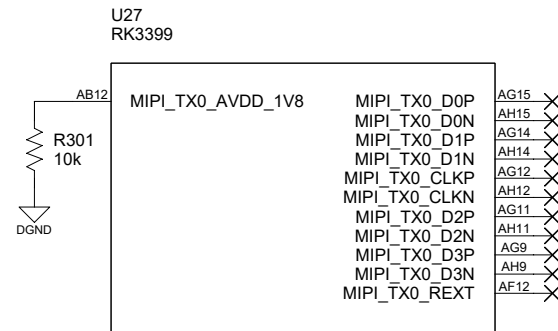
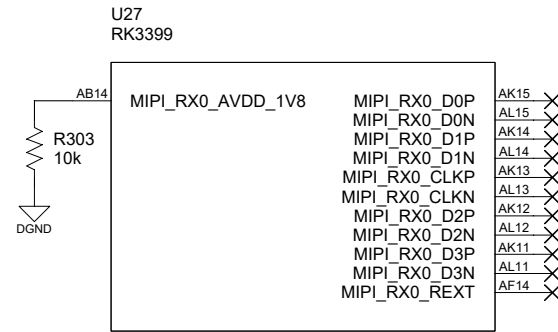
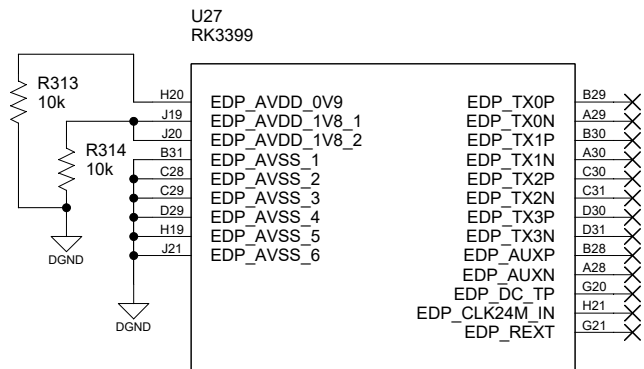
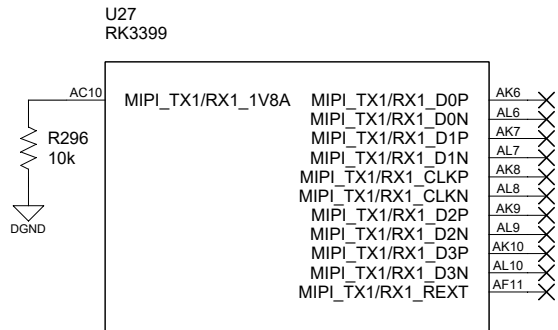
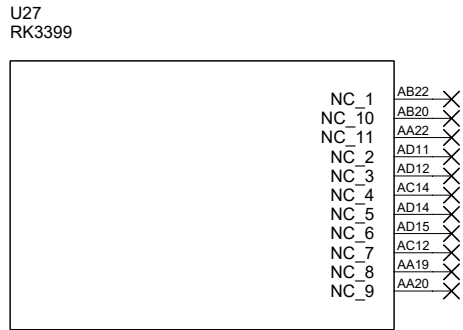
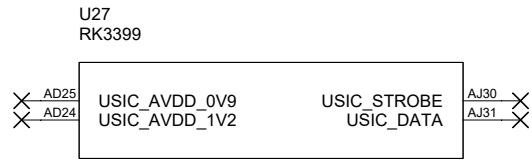
GPIO1_A0/ISP0-1_SHEN/TCPD_VB_SINK_EN	d	R25	HDD_A_ON
GPIO1_A1/ISP0-1_SHTR/TCPD_CC0_VC_EN	d	T31	HDD_B_ON
GPIO1_A2/ISP0-1_FLTRIN/TCPD_CC1_VC_EN	d	R26	TYPEC0_CC_INT_L
GPIO1_A3/ISP0-1_FLASHTRIGOUT	d	R27	VCC5V0_TYPEC0_EN
GPIO1_A4/ISP0-1_PRELIGHT_TRIG	d	R28	USB_MUX_HS
GPIO1_A5/AP_PWROFF	d	R30	PMIC_SLEEP_H
GPIO1_A6/TSADC_INT	d	P26	OTP_OUT_H
GPIO1_A7/SPI1_RXD/UART4_RX	u	P27	SOC_SO
GPIO1_B0/SPI1_TXD/UART4_TX	u	R31	SOC_SI
GPIO1_B1/SPI1_CLK/PMCU_JTAG_TCK	u	P28	
GPIO1_B2/SPI1_CSN0/PMCU_JTAG_TMS	u	P29	
GPIO1_B3/I2C4_SDA	u	P31	I2C4_SDA_TYPEC
GPIO1_B4/I2C4_SCL	u	P30	I2C4_SCL_TYPEC
GPIO1_B5	u	M24	USB_MUX_OE
GPIO1_B6/PWM3B_IR	d	M25	GPU_SLEEP_H
GPIO1_B7/SPI3_RXD/I2C0_SDA	u	M26	I2C0_SDA_PMIC
GPIO1_C0/SPI3_TXD/I2C0_SCL	u	N30	I2C0_SCL_PMIC
GPIO1_C1/SPI3_CLK	u	M27	CPU_SLEEP_B_H
GPIO1_C2/SPI3_CSN0	u	N31	SOC_WP#
GPIO1_C3/PWM2	d	M28	LOG_DVS_PWM
GPIO1_C4/I2C8_SDA	u	M29	I2C8_SDA
GPIO1_C5/I2C8_SCL	u	M30	I2C8_SCL
GPIO1_C6/TCPD_VBUS_SOURCE0	d	L25	USB3_EN
GPIO1_C7/TCPD_VBUS_SOURCE1	d	M31	USB3LAN_PWR
GPIO1_D0/TCPD_VBUS_SOURCE2	d	L26	PCIE_PWR
DFTJTAG_TMS		AA24	
DFTJTAG_TRSTN		AB24	
PMUIO2_VDDPST		N23	
PMUIO2_VDD		P23	



SIZE	DWG NO	SHEET of Variant		REV
A4	Helios64 NAS	31	37	1.2
DRAWN BY D.Sidorovich		SHEET of Variant		soc_pwrctr_ovp
CHECKED BY Gauthier Provost Aditya Prayoga		SJK		
			05/16/2020	

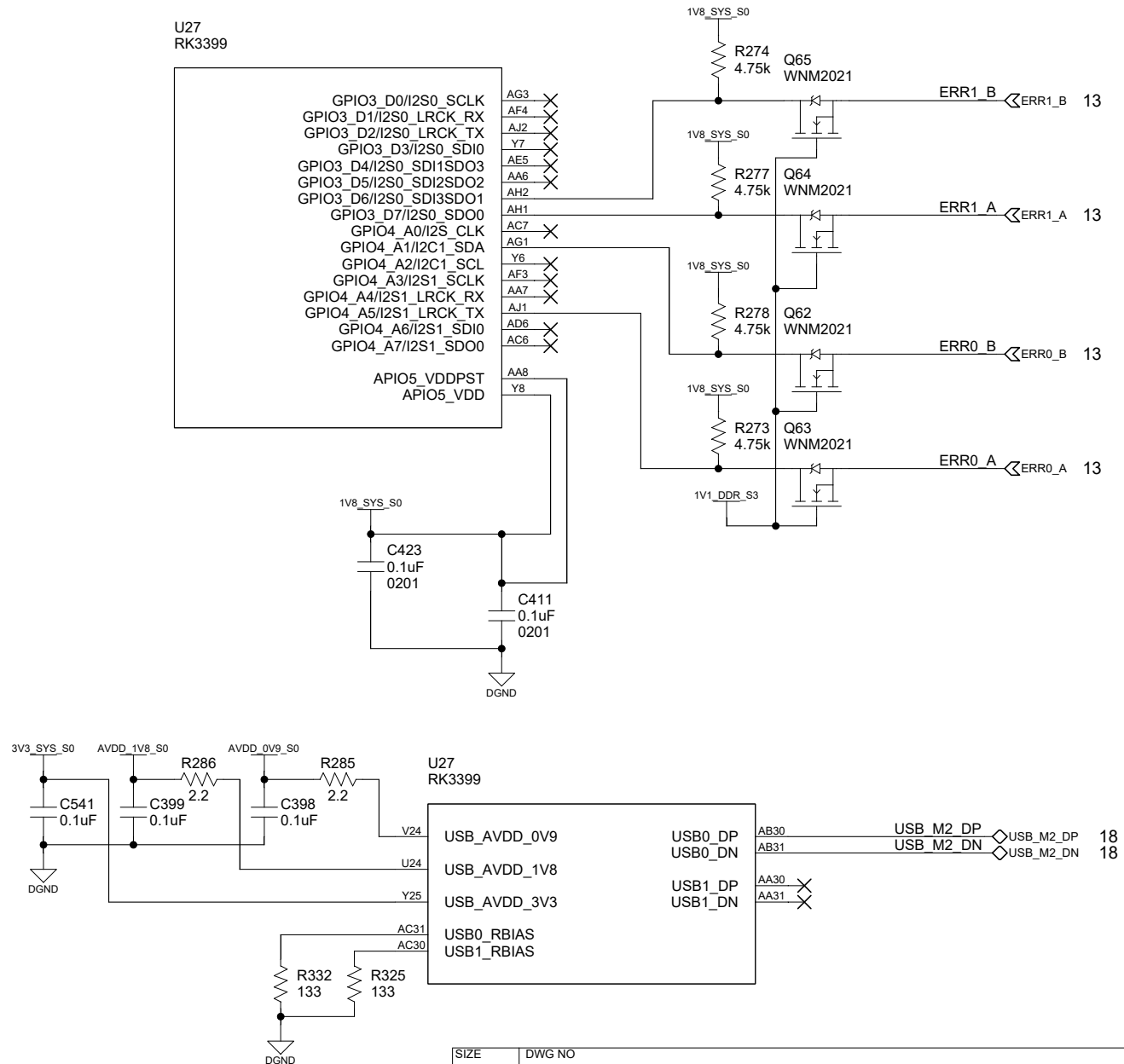
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Unused SoC Peripheral



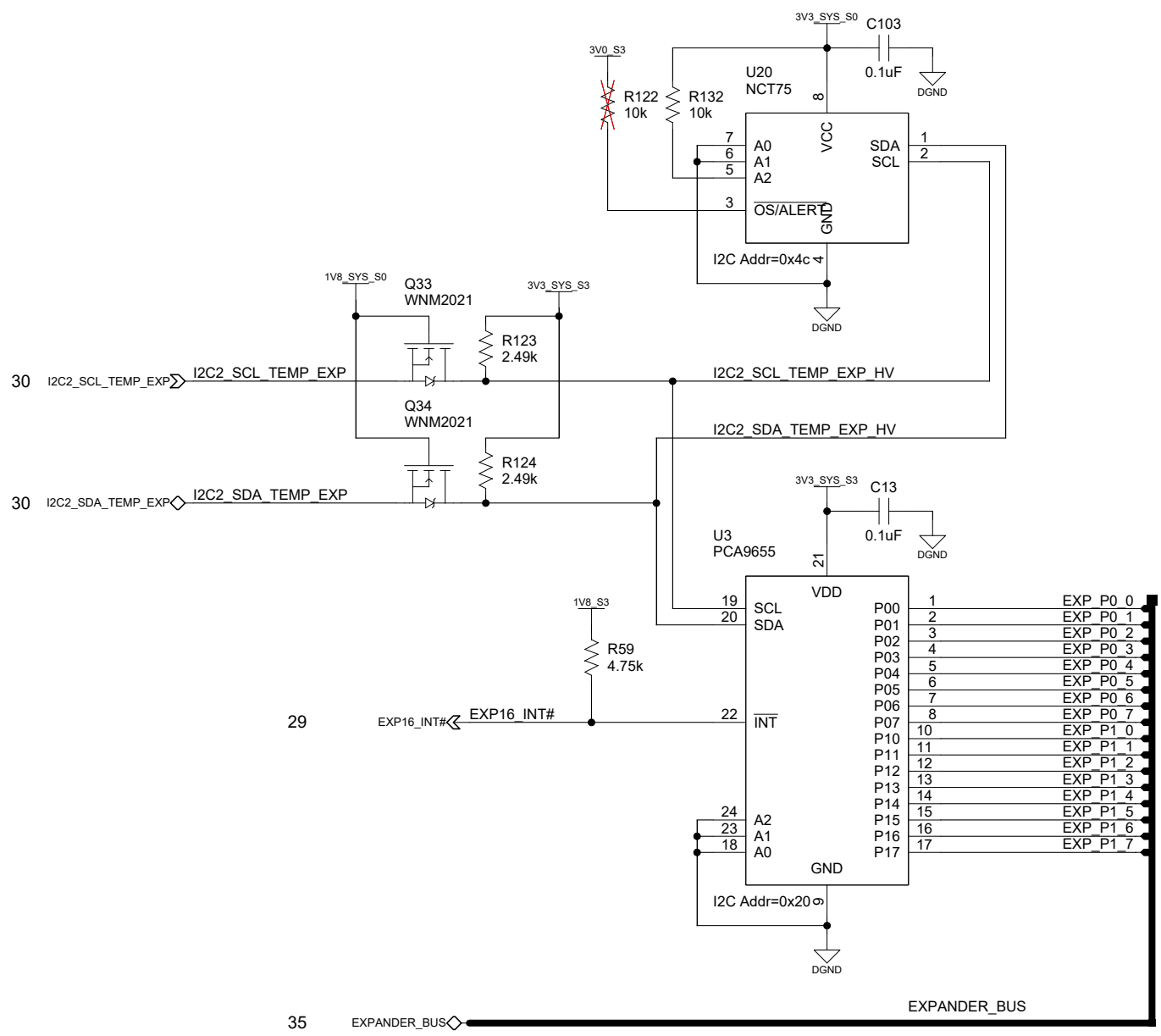
SIZE	DWG NO	SHEET of Variant		REV
A4	Helios64 NAS	32	37	SJK
DRAWN BY CHECKED BY		D.Sidorovich Gauthier Provost Aditya Prayoga		05/07/2020
				soc_unused 1.2

ECC LPDDR Support and USB2.0 M.2



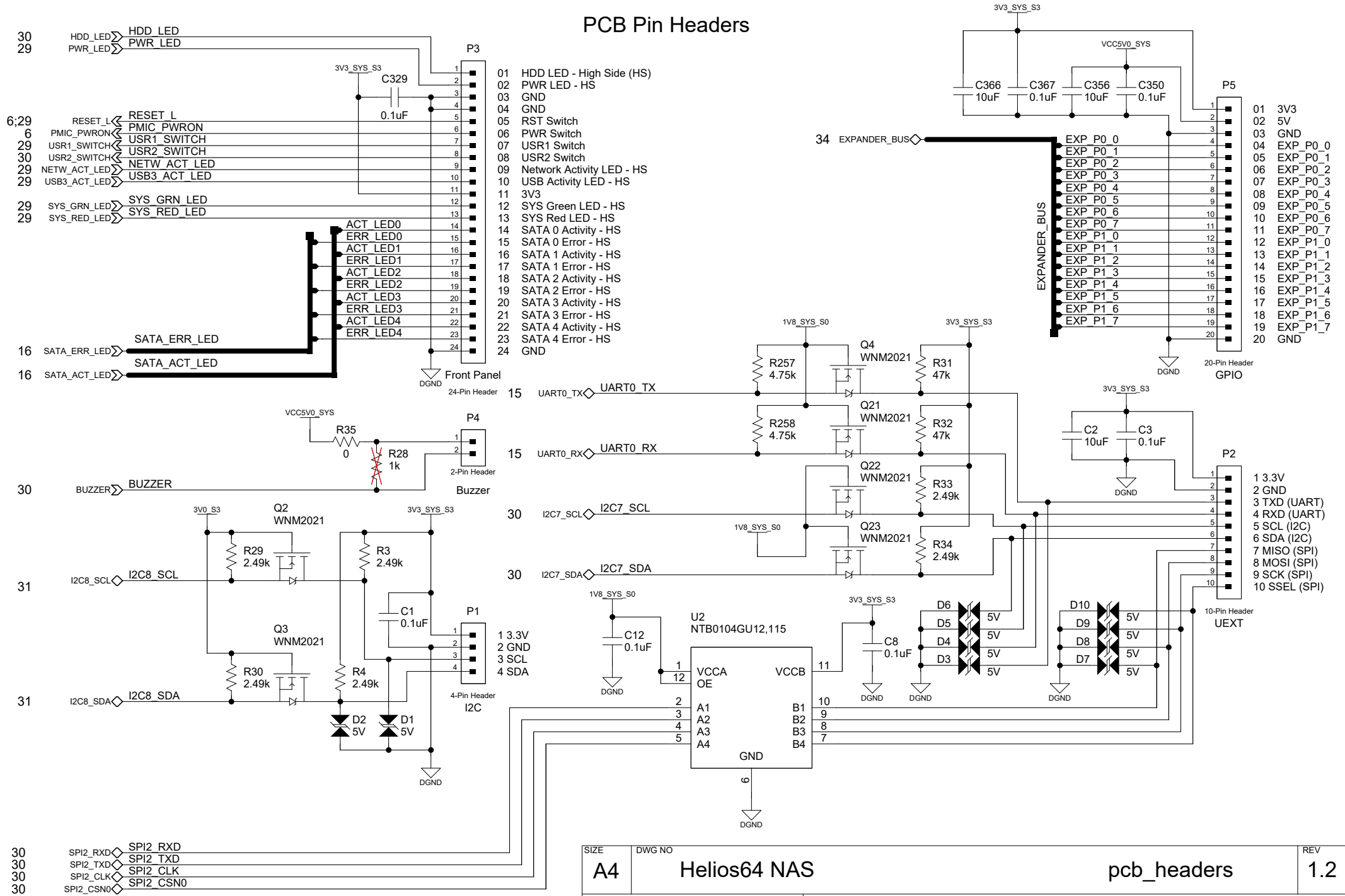
SIZE	DWG NO			REV
A4	Helios64 NAS	ecc_usb2_m2		1.2
DRAWN BY	D.Sidorovich	SHEET	of	Variant
CHECKED BY	Gauthier Provost Aditya Prayoga	33	37	SJK
			05/10/2020	

I2C Expander / Temperature Sensor



SIZE	DWG NO			REV
A4	Helios64 NAS	temp_expander		1.2
DRAWN BY	D.Sidorovich	SHEET	of	Variant
CHECKED BY	Gauthier Provost Aditya Prayoga	34	37	SJK
			05/10/2020	

PCB Pin Headers

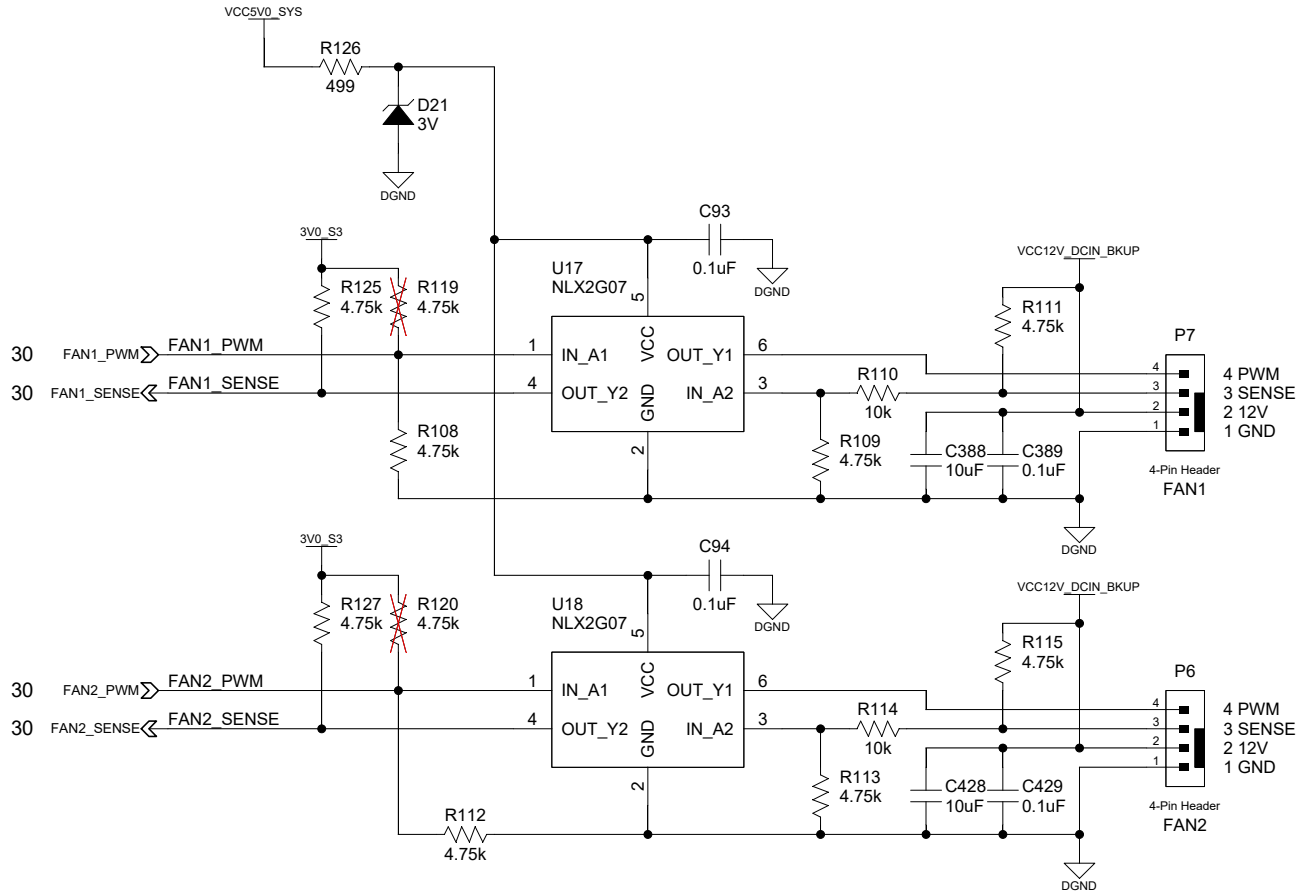


- 01 HDD LED - High Side (HS)
- 02 PWR LED - HS
- 03 GND
- 04 GND
- 05 RST Switch
- 06 PWR Switch
- 07 USB1 Switch
- 08 USB2 Switch
- 09 Network Activity LED - HS
- 10 USB Activity LED - HS
- 11 3V3
- 12 SYS Green LED - HS
- 13 SYS Red LED - HS
- 14 SATA 0 Activity - HS
- 15 SATA 0 Error - HS
- 16 SATA 1 Activity - HS
- 17 SATA 1 Error - HS
- 18 SATA 2 Activity - HS
- 19 SATA 2 Error - HS
- 20 SATA 3 Activity - HS
- 21 SATA 3 Error - HS
- 22 SATA 4 Activity - HS
- 23 SATA 4 Error - HS
- 24 GND

- 01 3V3
- 02 5V
- 03 GND
- 04 EXP_P0_0
- 05 EXP_P0_1
- 06 EXP_P0_2
- 07 EXP_P0_3
- 08 EXP_P0_4
- 09 EXP_P0_5
- 10 EXP_P0_6
- 11 EXP_P0_7
- 12 EXP_P1_0
- 13 EXP_P1_1
- 14 EXP_P1_2
- 15 EXP_P1_3
- 16 EXP_P1_4
- 17 EXP_P1_5
- 18 EXP_P1_6
- 19 EXP_P1_7
- 20 GND

SIZE	DWG NO	REV	
A4	Helios64 NAS	pcb_headers	1.2
DRAWN BY	D.Sidorovich	SHEET	of
CHECKED BY	Gauthier Provost Aditya Prayoga	35	37
		Variant	SJK
		05/14/2020	

FAN Connectors



SIZE	DWG NO			REV
A4	Helios64 NAS	fans_headers		1.2
DRAWN BY	D.Sidorovich	SHEET	of	Variant
CHECKED BY	Gauthier Provost Aditya Prayoga	36	37	SJK
		05/13/2020		

I2C Map

I2C	0	1	2	3	4	5	6	7	8
RK808-D	0x1b								
SYR837	0x40								
SYR838	0x41								
NCT75			0x4c						
PCA9655			0x20						
FUSB302					0x22				
P1 Header									Ext Devices
P2 Header				Reserved (UART default)			Reserved (SPI default)	Ext Devices	
Absent		Reserved							

SIZE	A4	DWG NO	Helios64 NAS		SHEET	37	of	37	Variant	SJK	REV	1.2
DRAWN BY	D.Sidorovich		i2c_map		05/07/2020							
CHECKED BY	Gauthier Provost Aditya Prayoga											